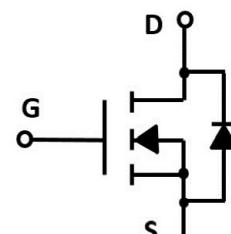


Features

- High density cell design for low $R_{DS(ON)}$
- Voltage controlled small signal switch
- Rugged and reliable
- High saturation current capability

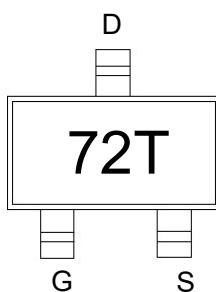
Product Summary

V_{DS}	$R_{DS(ON)} \text{ MAX}$	$I_D \text{ MAX}$
60V	5Ω@10V	0.3A
	7Ω@4.5V	



SOT-523 top view

Schematic diagram



72T: Device code



Halogen-Free

Marking and pin assignment

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Symbol	Parameter	Rating	Unit
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Common Ratings (TC=25°C Unless Otherwise Noted)

V_{DS}	Drain-Source Breakdown Voltage	60	V
V_{GS}	Gate-Source Voltage	±20	V
T_J	Maximum Junction Temperature	150	°C
T_{STG}	Storage Temperature Range	-50 to 155	°C
I_S	Diode Continuous Forward Current	Tc=25°C 0.3	A

Mounted on Large Heat Sink

I_{DM}	Pulse Drain Current Tested	Tc=25°C 0.8	A
I_D	Continuous Drain Current	Tc=25°C 0.3	A
P_D	Maximum Power Dissipation	Tc=25°C 0.15	W
$R_{θJA}$	Thermal Resistance Junction-to-Ambient	833	°C/W

Ordering Information (Example)

Type	Package	Marking	Minimum Package(pcs)	Inner Box Quantity(pcs)	Outer Carton Quantity(pcs)	Delivery Mode
2N7002T	SOT-523	72T	3,000	45,000	180,000	7" reel

Electrical Characteristics (TJ=25°C unless otherwise noted)						
Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ TJ = 25°C (unless otherwise stated)						
BV _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	60	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =60V, V _{GS} =0V	--	--	1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V	--	--	±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1.0	1.6	2.5	V
R _{DS(on)}	Drain-Source On-State Resistance	V _{GS} =10V, I _D =0.5A	--	0.85	5.0	Ω
		V _{GS} =5V, I _D =0.05A	--	1.0	7.0	Ω
Dynamic Electrical Characteristics @ TJ = 25°C (unless otherwise stated)						
C _{ISS}	Input Capacitance	V _{DS} =30V, V _{GS} =0V, f=1MHz	--	34	--	pF
C _{OSS}	Output Capacitance		--	6	--	pF
C _{RSS}	Reverse Transfer Capacitance		--	2	--	pF
Switching Characteristics						
Q _g	Total Gate Charge	V _{DS} =30V, I _D =0.3A, V _{GS} =10V	--	1.7	--	nC
Q _{gs}	Gate-Source Charge		--	0.57	--	nC
Q _{gd}	Gate-Drain Charge		--	0.3	--	nC
t _{d(on)}	Turn-on Delay Time	V _{DD} =30V, I _D =0.3A, V _{GS} =10V, R _G =3Ω	--	4	--	nS
t _r	Turn-on Rise Time		--	19	--	nS
t _{d(off)}	Turn-Off Delay Time		--	12	--	nS
t _f	Turn-off fall Time		--	24	--	nS
Source- Drain Diode Characteristics						
V _{SD}	Forward on voltage	T _j =25°C, I _S =0.3A	--	--	1.2	V

Typical Operating Characteristics

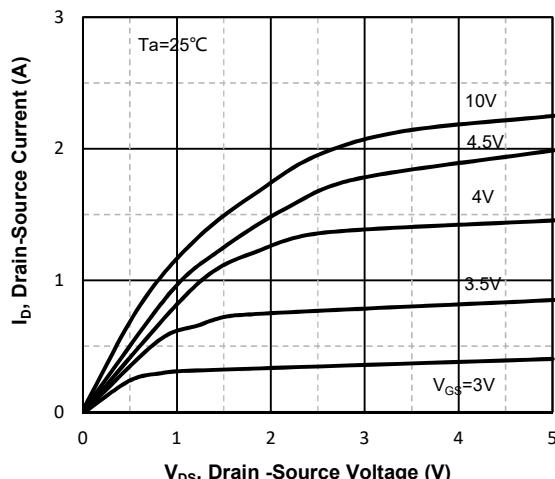


Fig1. Typical Output Characteristics

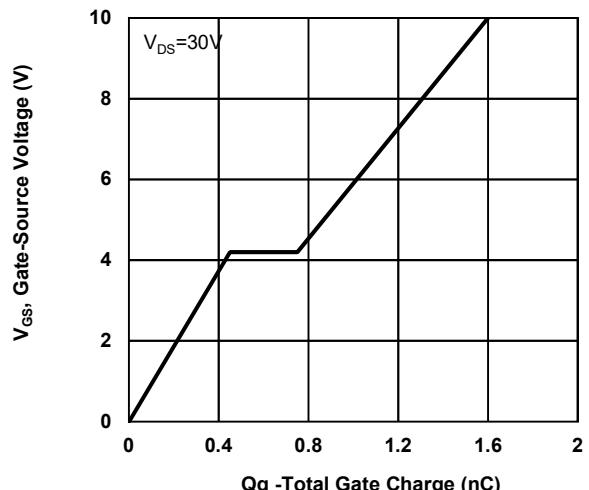


Fig2. Typical Gate Charge Vs. Gate-Source Voltage

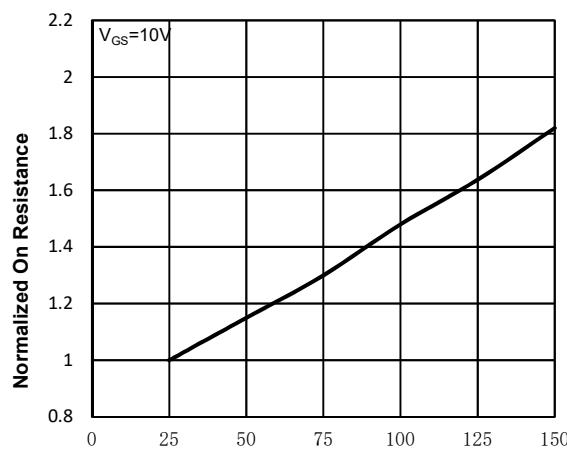


Fig3. Normalized On-Resistance Vs. Temperature

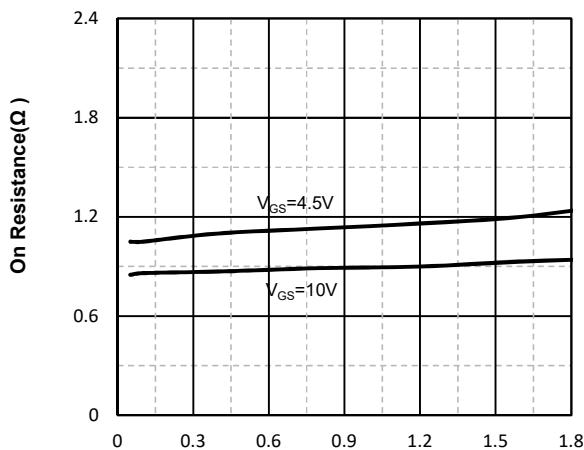


Fig4. On-Resistance Vs. Drain-Source Current

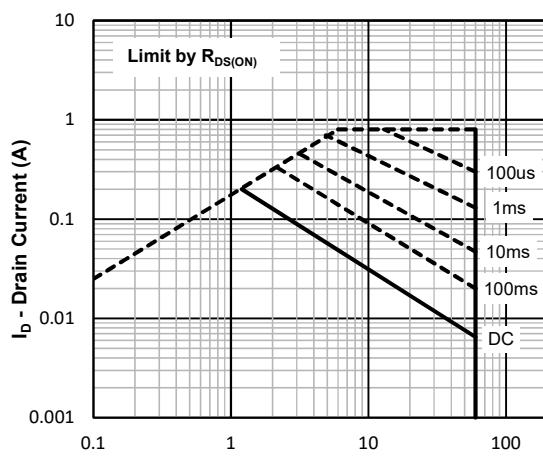


Fig5. Maximum Safe Operating Area

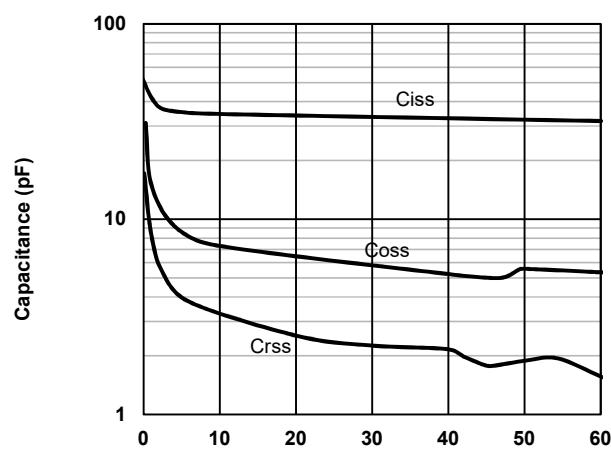
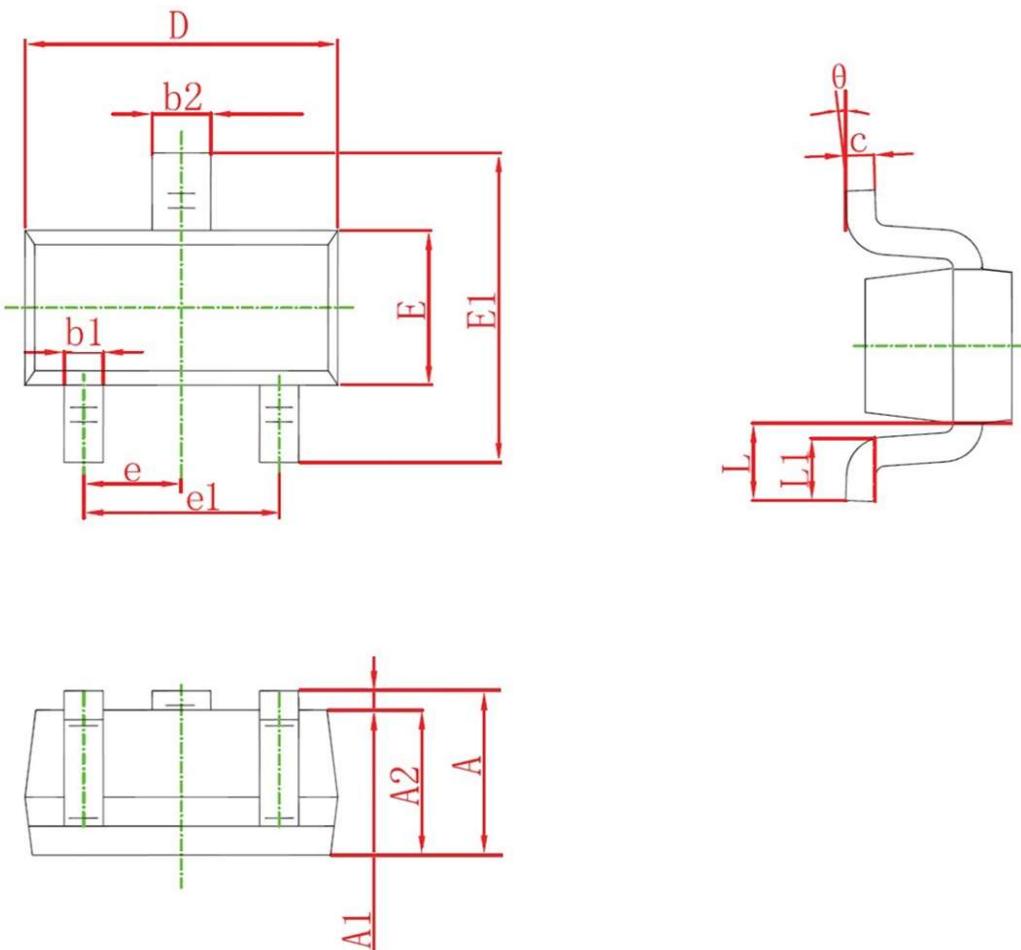


Fig6. Typical Capacitance Vs. Drain-Source Voltage

SOT-523 Package information



Symbol	Dimensions in Millimeters(mm)		Dimensions In Inches	
	Min	Max	Min	Max
A	0.700	0.900	0.028	0.035
A1	0.000	0.100	0.000	0.004
A2	0.700	0.800	0.028	0.031
b1	0.150	0.250	0.006	0.010
b2	0.250	0.350	0.010	0.014
c	0.100	0.200	0.004	0.008
D	1.500	1.700	0.059	0.067
E	0.700	0.900	0.028	0.035
E1	1.450	1.750	0.057	0.069
e	0.500TYP		0.020TYP	
e1	0.900	1.100	0.035	0.043
L	0.400REF		0.016REF	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°