IRF9310

-30V/-20A P-Channel MOSFET

Features

- Lower Conduction Losses
- Multi-Vendor Compatibility
- 100% avalanche tested
- Environmentally Friendlier

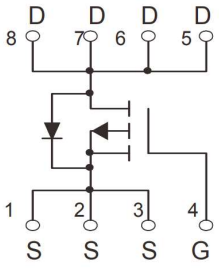
Product Summary

V_{DS}	$R_{DS(ON)}$ MAX	I_D MAX
-30	4.6mΩ@-10V	-20
	6.8mΩ@-4.5V	

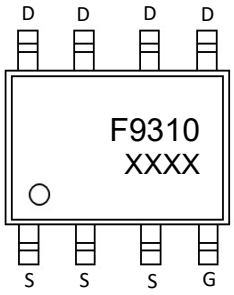
Application

- Switch Mode Power Supply (SMPS)
- Motor Controls

SOP-8 top view



Schematic diagram



F9310 : Device code
XXXX : Code

Marking and pin assignment

Pb-Free RoHS Halogen-Free

Absolute Maximum Ratings (TA=25℃unless otherwise noted)

Symbol	Parameter		Rating	Unit
Common Ratings (TC=25°C Unless Otherwise Noted)				
V_{DS}	Drain-Source Breakdown Voltage		-30	V
V_{GS}	Gate-Source Voltage		±20	V
T_J	Maximum Junction Temperature		150	°C
T_{STG}	Storage Temperature Range		-50 to 155	°C
I_S	Diode Continuous Forward Current	Tc=25°C	-20	A
Mounted on Large Heat Sink				
I_{DM}	Pulse Drain Current Tested	Tc=25°C	-160	A
I_D	Continuous Drain Current	Tc=25°C	-20	A
P_D	Maximum Power Dissipation	Tc=25°C	2.5	W
$R_{\theta JA}$	Thermal Resistance Junction-to-Ambient		83	°C/W

Ordering Information (Example)

Type	Package	Marking	Minimum Package(pcs)	Inner Box Quantity(pcs)	Outer Carton Quantity(pcs)	Delivery Mode
IRF9310	SOP-8	F9310	3,000	6,000	42,000	13"reel

Electrical Characteristics (TJ=25℃ unless otherwise noted)						
Symbol	**Parameter**	**Condition**	**Min**	**Typ**	**Max**	**Unit**
Static Electrical Characteristics @ TJ = 25°C (unless otherwise stated)						
$BV_{(BR)DSS}$	Drain-Source Breakdown Voltage	V_{GS}=0V，I_D=250μA	-30	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=-30V，V_{GS}=0V	--	--	-1	μA
I_{GSS}	Gate-Body Leakage Current	V_{GS}=±20V，V_{DS}=0V	--	--	±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}，I_D=-250μA	-1.3	-1.8	-2.4	V
$R_{DS(on)}$	Drain-Source On-State Resistance	V_{GS}=-10V， I_D=-20A	--	3.9	4.6	mΩ
		V_{GS}=-4.5V，I_D=-16A	--	5.8	6.8	mΩ
Dynamic Electrical Characteristics @ TJ = 25°C (unless otherwise stated)						
C_{ISS}	Input Capacitance	V_{DS}=-15V，V_{GS}=0V， f=1MHz	--	5500	--	pF
C_{OSS}	Output Capacitance		--	720	--	pF
C_{RSS}	Reverse Transfer Capacitance		--	500	--	pF
Switching Characteristics						
Q_g	Total Gate Charge	V_{DS}=-10V，I_D=-20A，V_{GS}=-15V	--	30	--	nC
Q_{gs}	Gate Source Charge		--	5.5	--	nC
Q_{gd}	Gate Drain Charge		--	7.5	--	nC
$t_{d(on)}$	Turn-on Delay Time	V_{DD}=-15V，I_D=-20A，V_{GS}=-10V，R_G=25Ω	--	13	--	nS
t_r	Turn-on Rise Time		--	20	--	nS
$t_{d(off)}$	Turn-Off Delay Time		--	90	--	nS
t_f	Turn-Off Fall Time		--	65	--	nS
Source- Drain Diode Characteristics						
V_{SD}	Forward on voltage	Tj=25℃，Is=-20A	--	--	1.2	V

Typical Operating Characteristics

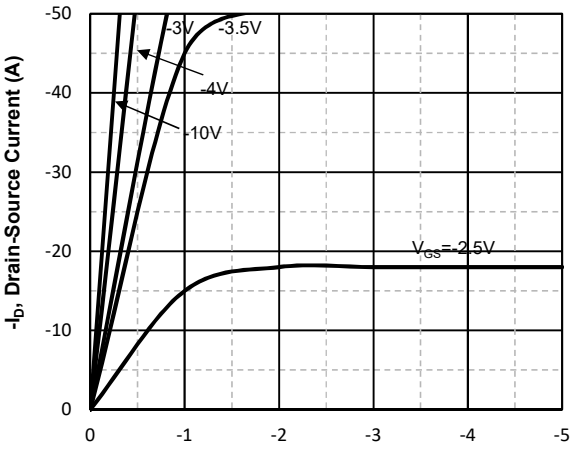


Fig1. Typical Output Characteristics

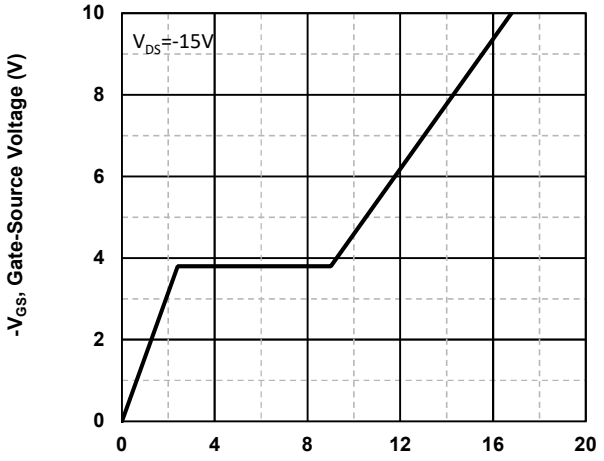


Fig2. Typical Gate Charge Vs.Gate-Source Voltage

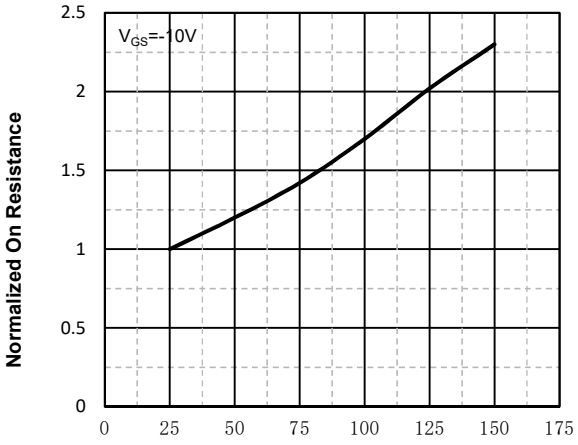


Fig3. Normalized On-Resistance Vs. Temperature

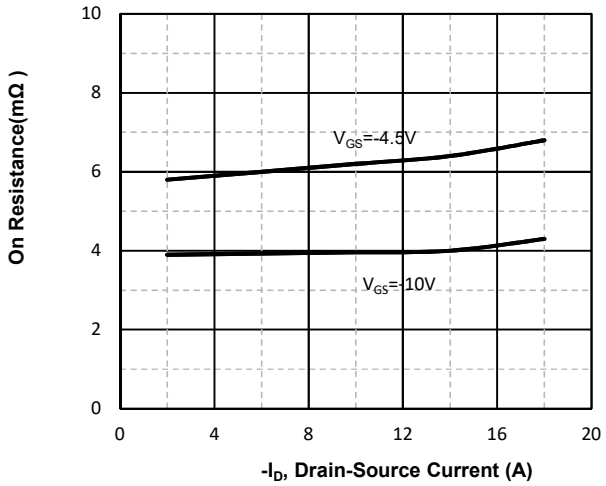


Fig4. On-Resistance Vs. Drain-Source Current

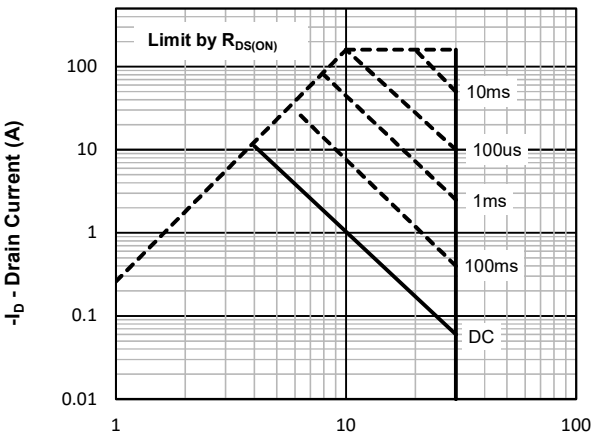


Fig5. Maximum Safe Operating Area

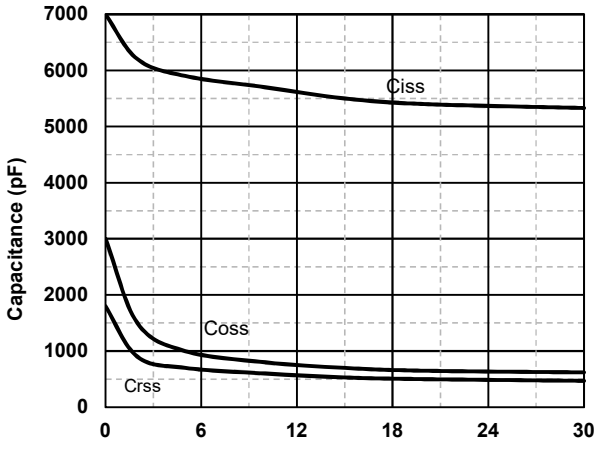
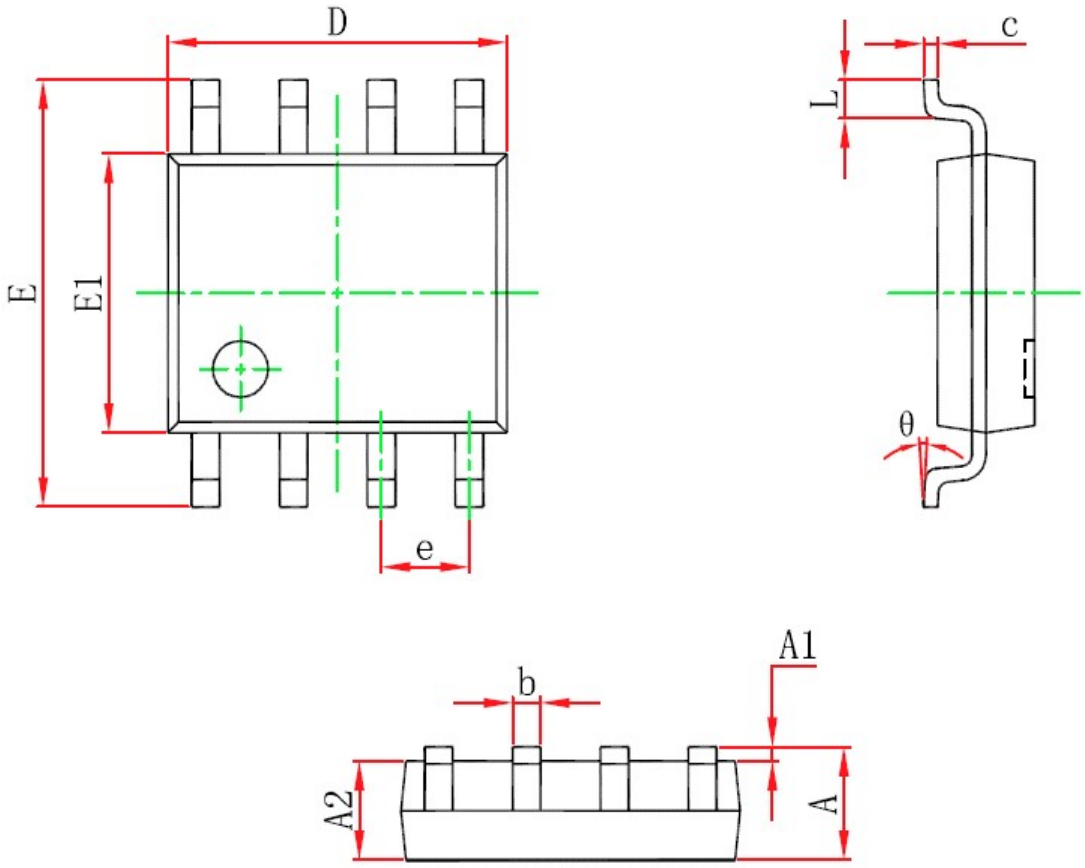


Fig6 Typical Capacitance Vs.Drain-Source Voltage

SOP-8 Package information



Symbol	Dimensions in Millimeters(mm)		Dimensions In Inches	
	Min	Max	Min	Max
A	1.450	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.700	5.100	0.185	0.201
e	1.270(BSC)		0.050(BSC)	
E	5.800	6.200	0.028	0.035
E1	3.800	4.000	0.057	0.069
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°