

## Features

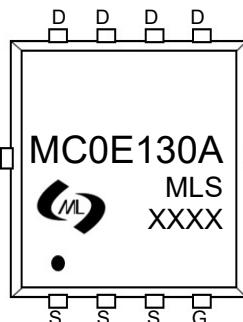
- High density cell design for ultra low  $R_{DS(ON)}$
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high  $E_{AS}$
- Excellent package for good heat dissipation

## Product Summary

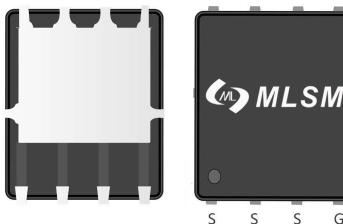
$V_{DS}$	$R_{DS(ON)} \text{ MAX}$	$I_D \text{ MAX}$
40V	3.5mΩ@10V	130A
	5.5mΩ@4.5V	

## Application

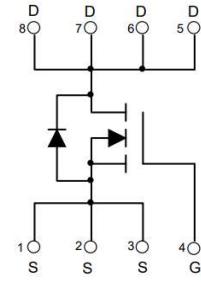
- Power switching application



MC0E130A: Device code  
XXXX : Code



PDFN5X6-8L view



Schematic diagram



Halogen-Free

Marking and pin assignment

## Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Symbol	Parameter	Rating	Unit
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## Common Ratings (TC=25°C Unless Otherwise Noted)

$V_{DS}$	Drain-Source Breakdown Voltage	40	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$T_J$	Maximum Junction Temperature	150	°C
$T_{STG}$	Storage Temperature Range	-50 to 155	°C
$I_S$	Diode Continuous Forward Current	Tc=25°C	130
			A

## Mounted on Large Heat Sink

$I_{DM}$	Pulse Drain Current Tested	Tc=25°C	520	A
$I_D$	Continuous Drain Current	Tc=25°C	130	A
$P_D$	Maximum Power Dissipation	Tc=25°C	110	W
$R_{θJA}$	Thermal Resistance Junction-Ambient		20	°C/W

## Ordering Information (Example)

Type	Package	Marking	Minimum Package(pcs)	Inner Box Quantity(pcs)	Outer Carton Quantity(pcs)	Delivery Mode
MC0E130A	PDFN5X6-8L	MC0E130A	5,000	10,000	70,000	13" reel

**Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise noted)**

Symbol	Parameter	Condition	Min	Typ	Max	Unit
<b>Static Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
BV <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	40	--	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V	--	--	1	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	--	--	±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.0	--	2.5	V
R <sub>DS(on)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	--	2.8	3.5	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =15A	--	4	5.5	mΩ

**Dynamic Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)**

C <sub>ISS</sub>	Input Capacitance	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V, f=1MHz	--	4656	--	pF
C <sub>OSS</sub>	Output Capacitance		--	436	--	pF
C <sub>RSS</sub>	Reverse Transfer Capacitance		--	360	--	pF

**Switching Characteristics**

Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =20V, I <sub>D</sub> =20A, V <sub>GS</sub> =10V	--	102	--	nC
Q <sub>gs</sub>	Gate Source Charge		--	15.8	--	nC
Q <sub>gd</sub>	Gate Drain Charge		--	21.9	--	nC
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DS</sub> =20V, I <sub>D</sub> =20A, V <sub>GS</sub> =10V, R <sub>G</sub> =3Ω	--	12	--	nS
t <sub>r</sub>	Turn-on Rise Time		--	54	--	nS
t <sub>d(off)</sub>	Turn-Off Delay Time		--	120	--	nS
t <sub>f</sub>	Turn-Off Fall Time		--	80	--	nS

**Source- Drain Diode Characteristics**

V <sub>SD</sub>	Forward on voltage	T <sub>j</sub> =25°C, I <sub>s</sub> =10A	--	0.8	1.2	V
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### Typical Operating Characteristics

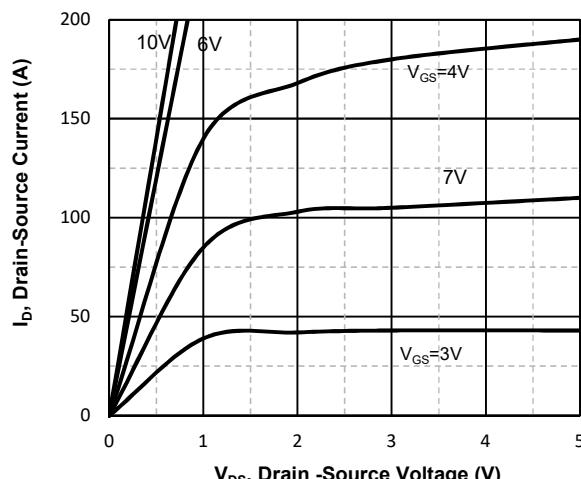


Fig1. Typical Output Characteristics

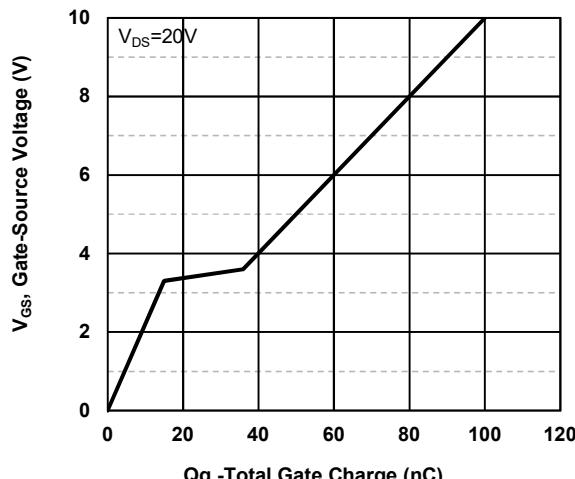


Fig2. Typical Gate Charge Vs.Gate-Source Voltage

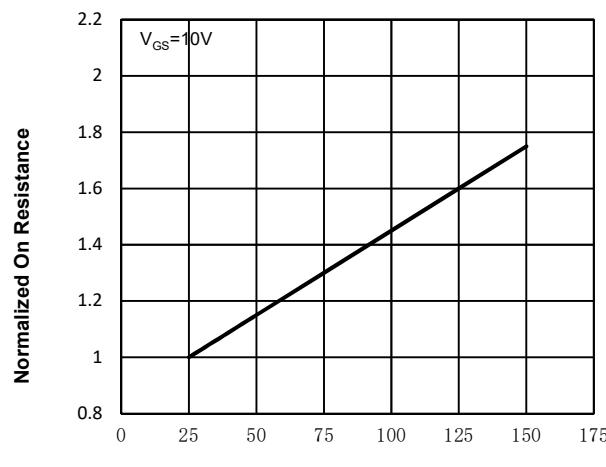


Fig3. Normalized On-Resistance Vs. Temperature

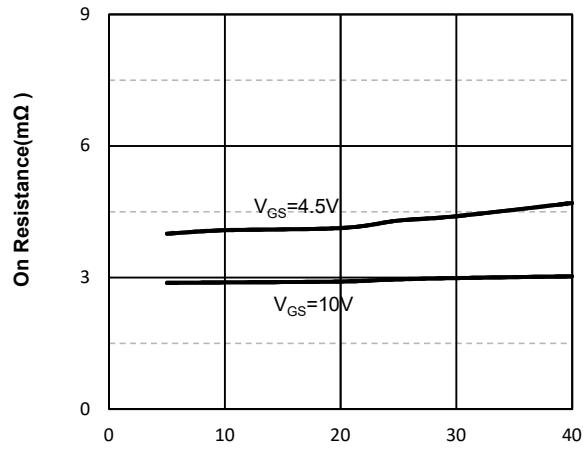


Fig4. On-Resistance Vs. Drain-Source Current

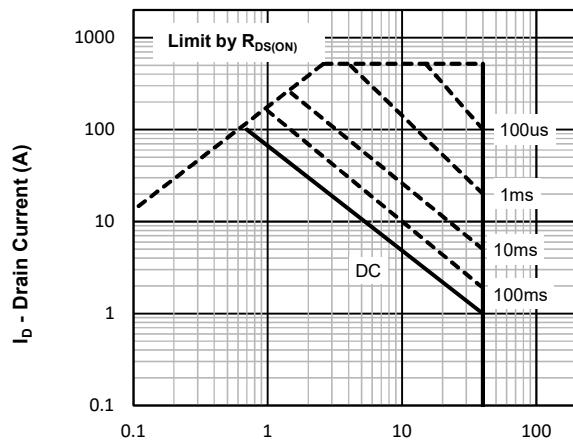


Fig5. Maximum Safe Operating Area

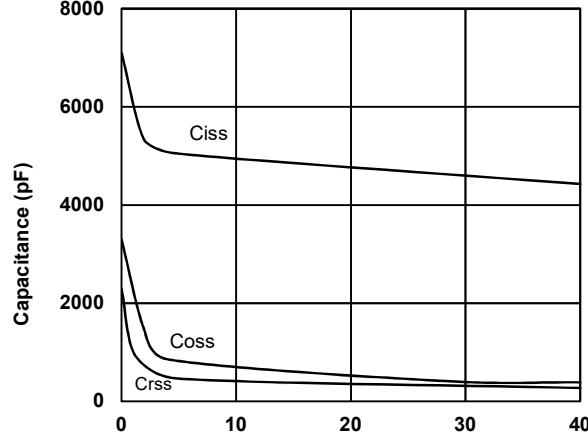
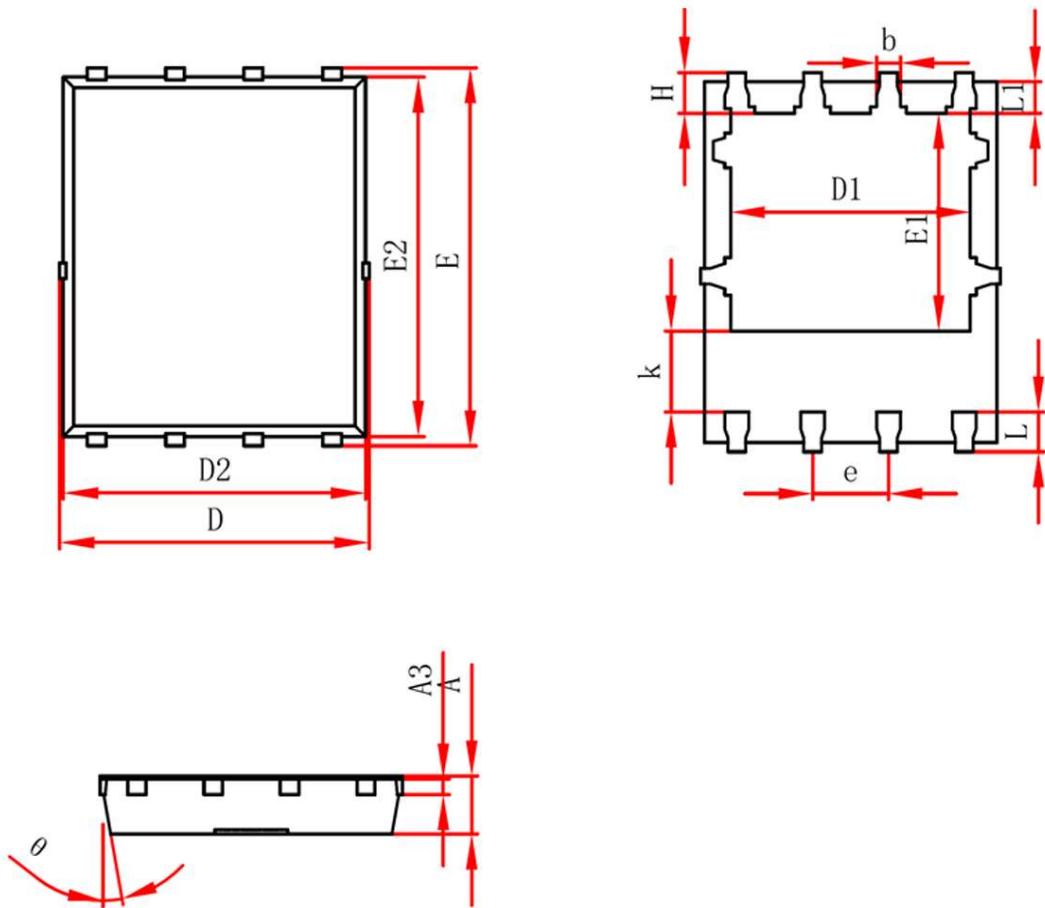


Fig6 Typical Capacitance Vs.Drain-Source

## PDFN5X6-8L Package information



Symbol	Dimensions in Millimeters(mm)		Dimensions In Inches	
	Min	Max	Min	Max
A	0.950	1.050	0.035	0.039
A3	0.254REF.		0.010REF.	
D	4.950	5.050	0.196	0.200
E	5.950	6.050	0.235	0.239
D1	4.026	4.126	0.159	0.163
E1	3.510	3.610	0.139	0.143
D2	4.850	4.950	0.192	0.196
E2	5.700	5.800	0.225	0.229
k	1.190	1.390	0.047	0.055
b	0.300	0.400	0.012	0.016
e	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
θ	10°	12°	10°	12°