

## Features

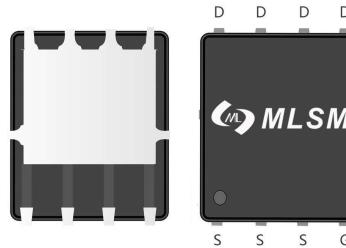
- Trench Power LV MOSFET technology
- High density cell design for Low  $R_{DS(ON)}$
- High Speed switching

## Product Summary

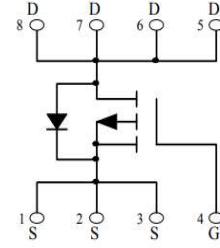
$V_{DS}$	$R_{DS(ON)} \text{ MAX}$	$I_D \text{ MAX}$
-60V	11mΩ@-10V	-80A
	16mΩ@-4.5V	

## Application

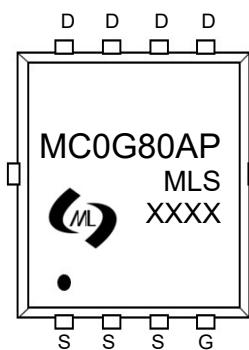
- PWM applications
- Power management
- Load switch



PDFN5X6-8L view



Schematic diagram



Marking and pin assignment

MC0G80AP: Device code  
XXXX:Code


Halogen-Free

## Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Symbol	Parameter	Rating	Unit	
<b>Common Ratings (TC=25°C Unless Otherwise Noted)</b>				
$V_{DS}$	Drain-Source Breakdown Voltage	-60	V	
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V	
$T_J$	Maximum Junction Temperature	150	°C	
$T_{STG}$	Storage Temperature Range	-55 to 150	°C	
$I_S$	Diode Continuous Forward Current	-80	A	
<b>Mounted on Large Heat Sink</b>				
$I_{DM}$	Pulse Drain Current Tested	Tc=25°C	-320	A
$I_D$	Continuous Drain Current	Tc=25°C	-80	A
$P_D$	Maximum Power Dissipation	Tc=25°C	68	W
$R_{\theta JA}$	Thermal Resistance Junction-to-Ambient		60	°C/W

## Ordering Information (Example)

Type	Package	Marking	Minimum Package(pcs)	Inner Box Quantity(pcs)	Outer Carton Quantity(pcs)	Delivery Mode
MC0G80AP	PDFN5X6-8L	MC0G80AP	5,000	10,000	70,000	13"reel

Electrical Characteristics (TJ=25°C unless otherwise noted)						
Symbol	Parameter	Condition	Min	Typ	Max	Unit
<b>Static Electrical Characteristics @ TJ = 25°C (unless otherwise stated)</b>						
$BV_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-60	-	--	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=-60V, V_{GS}=0V$	--	--	-1	$\mu A$
$I_{GSS}$	Gate-Body Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	--	--	$\pm 100$	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1	--	-2.5	V
$R_{DS(on)}$	Drain-Source On-State Resistance	$V_{GS}=-10V, I_D=-25A$	--	9	11	$m\Omega$
		$V_{GS}=-4.5V, I_D=-20A$	--	12	16	$m\Omega$
<b>Dynamic Electrical Characteristics @ TJ = 25°C (unless otherwise stated)</b>						
$C_{ISS}$	Input Capacitance	$V_{DS}=-30V, V_{GS}=0V, f=1MHz$	--	6981	--	pF
$C_{OSS}$	Output Capacitance		--	408	--	pF
$C_{RSS}$	Reverse Transfer Capacitance		--	327	--	pF
<b>Switching Characteristics</b>						
$Q_g$	Total Gate Charge	$V_{DS}=-30V, I_D=-25A, V_{GS}=-10V$	--	132	--	nC
$Q_{gs}$	Gate Source Charge		--	31	--	nC
$Q_{gd}$	Gate Drain Charge		--	25	--	nC
$t_{d(on)}$	Turn-on Delay Time	$V_{DD}=-10V, I_D=-25A, V_{GS}=-10V, R_G=3\Omega$	--	18	--	nS
$t_r$	Turn-on Rise Time		--	98	--	nS
$t_{d(off)}$	Turn-Off Delay Time		--	93	--	nS
$t_f$	Turn-Off Fall Time		--	113	--	nS
<b>Source-Drain Diode Characteristics</b>						
$V_{SD}$	Forward on voltage	$T_j=25^\circ C, I_S=-25A$	--	--	-1.2	V

### Typical Operating Characteristics

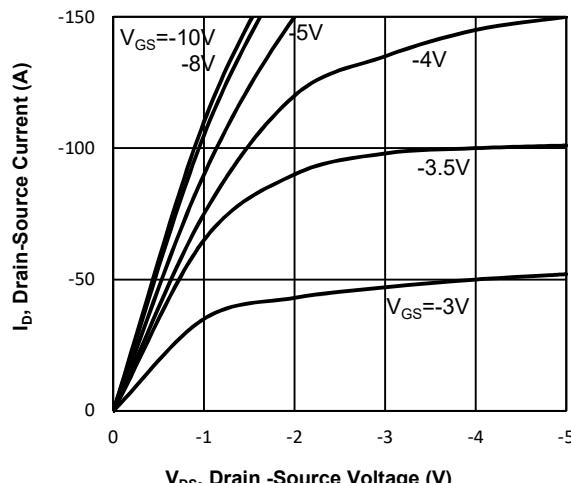


Fig1. Typical Output Characteristics

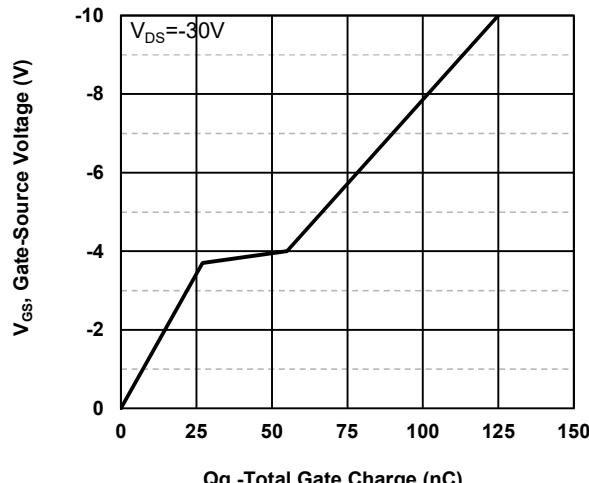


Fig2. Typical Gate Charge Vs.Gate-Source Voltage

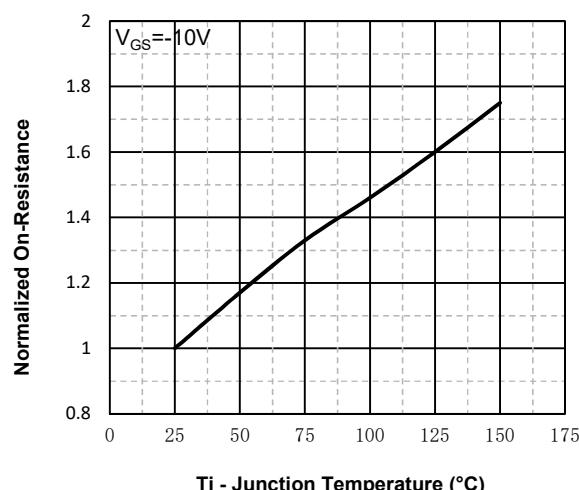


Fig3. Normalized On-Resistance Vs. Temperature

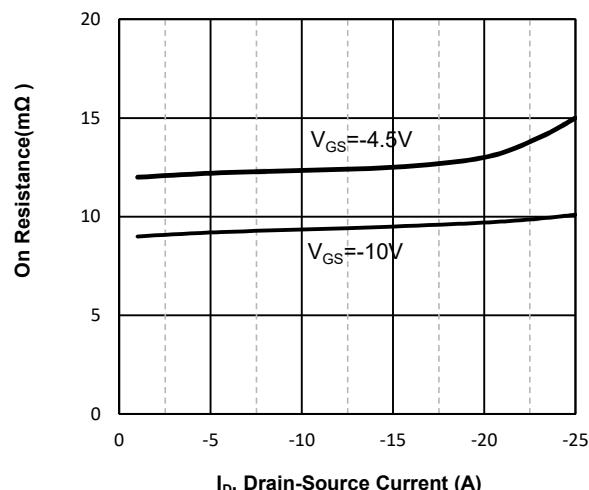


Fig4. on Resistance Vs.Drain-Source Current

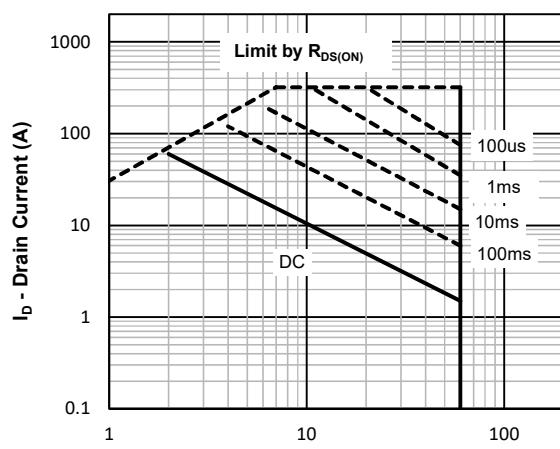


Fig5. Maximum Safe Operating Area

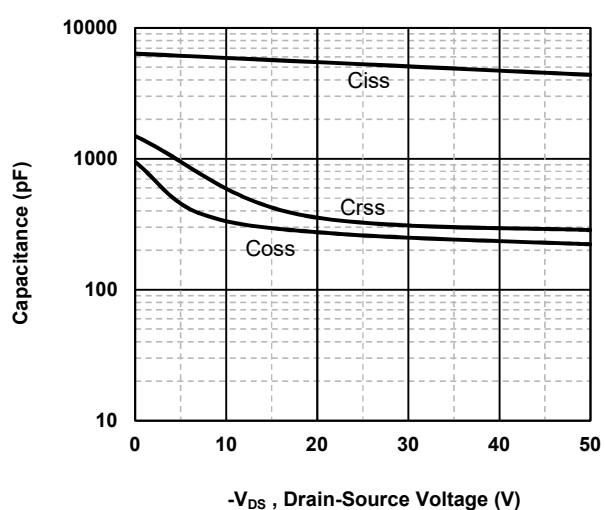
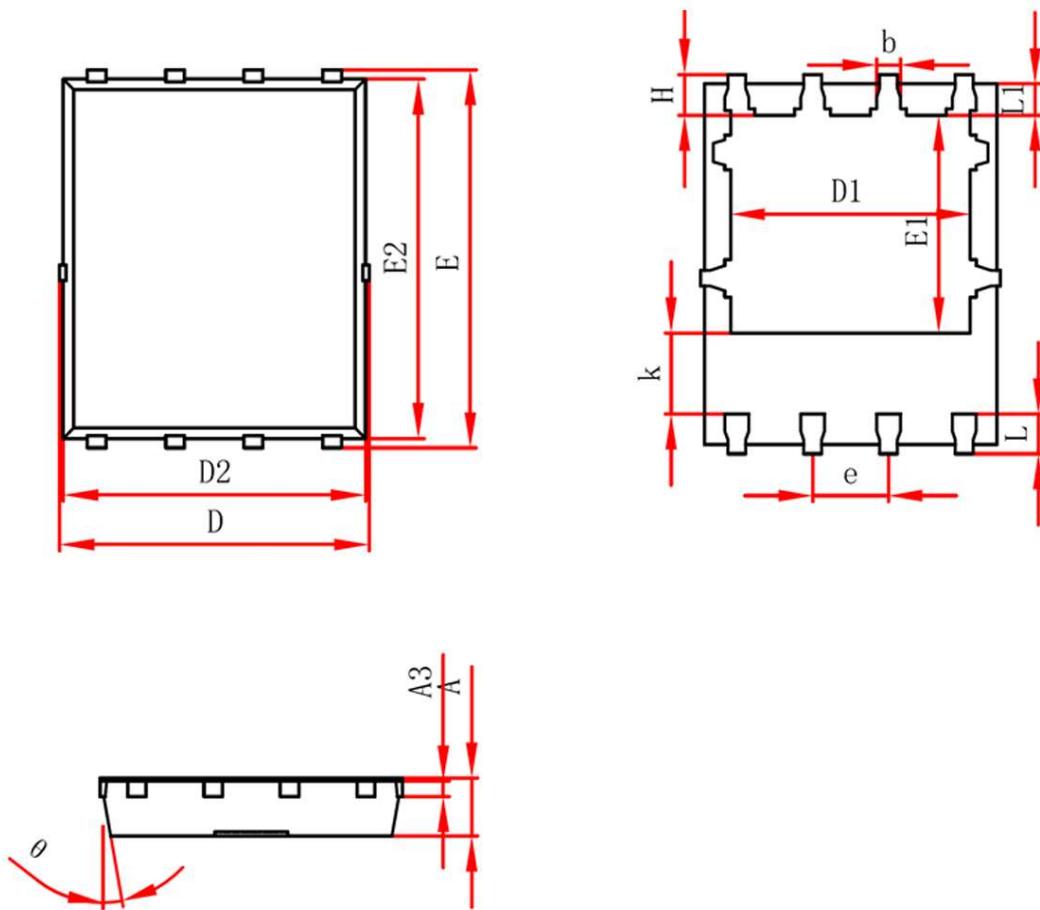


Fig6. Typical Capacitance Vs.Drain-Source Voltage

**PDFN5X6-8L Package information**


Symbol	Dimensions in Millimeters(mm)		Dimensions In Inches	
	Min	Max	Min	Max
A	0.950	1.050	0.035	0.039
A3	0.254REF.		0.010REF.	
D	4.950	5.050	0.196	0.200
E	5.950	6.050	0.235	0.239
D1	4.026	4.126	0.159	0.163
E1	3.510	3.610	0.139	0.143
D2	4.850	4.950	0.192	0.196
E2	5.700	5.800	0.225	0.229
k	1.190	1.390	0.047	0.055
b	0.300	0.400	0.012	0.016
e	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
θ	10°	12°	10°	12°