

### Features

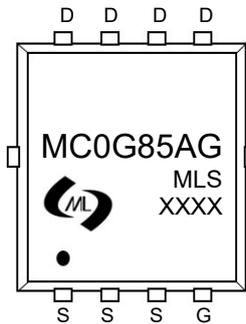
- Split gate trench MOSFET technology
- High density cell design for ultra low RDS(ON)
- Good stability and uniformity with high E<sub>AS</sub>
- Excellent package for good heat dissipation

### Application

- Power switching application
- Load Switch

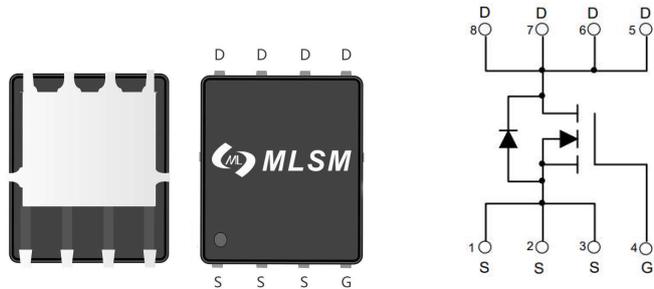
### Product Summary

V <sub>DS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> MAX
60V	6.2mΩ@10V	85A



MC0G85AG: Device code  
 XXXX : Code

Marking and pin assignment



PDFN5X6-8L view

Schematic diagram



Halogen-Free

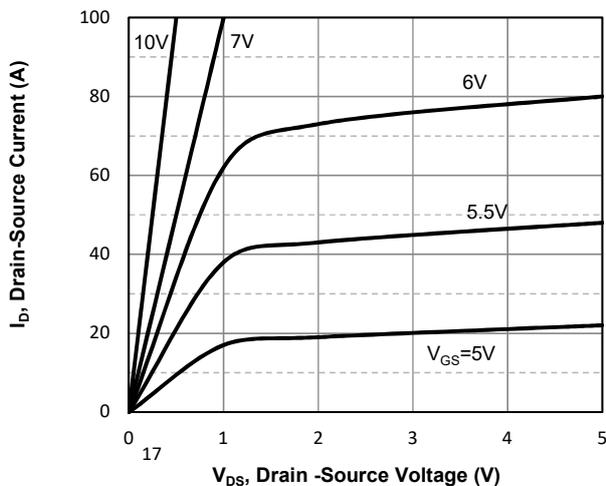
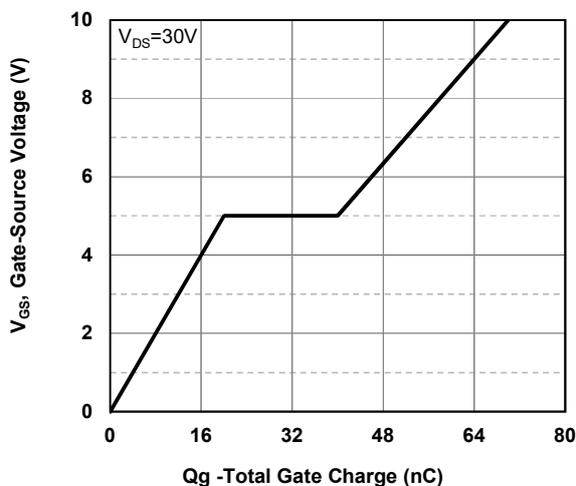
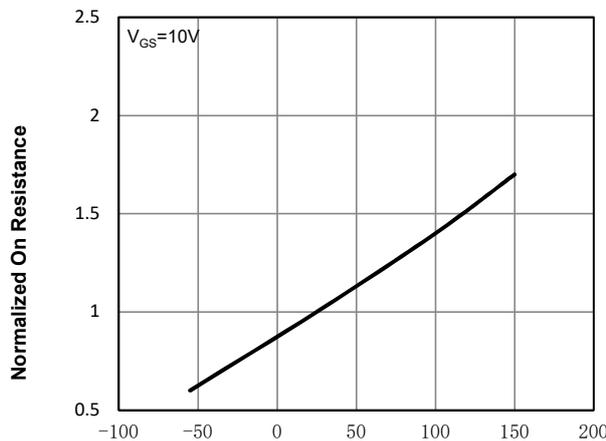
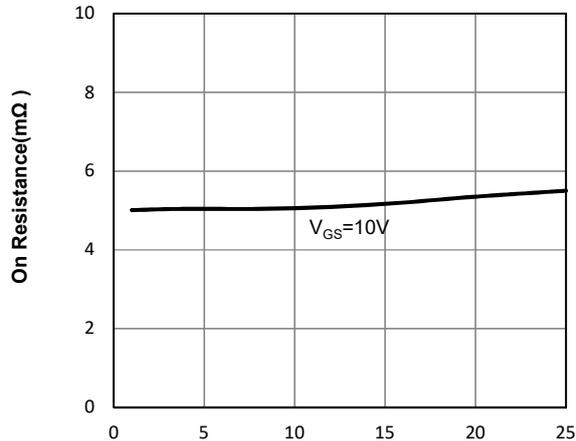
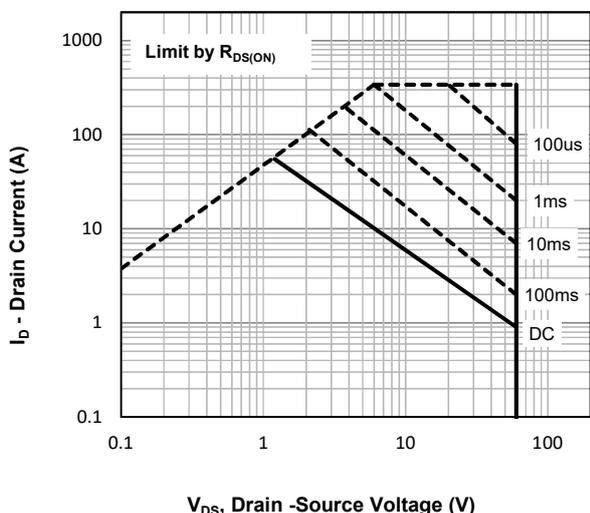
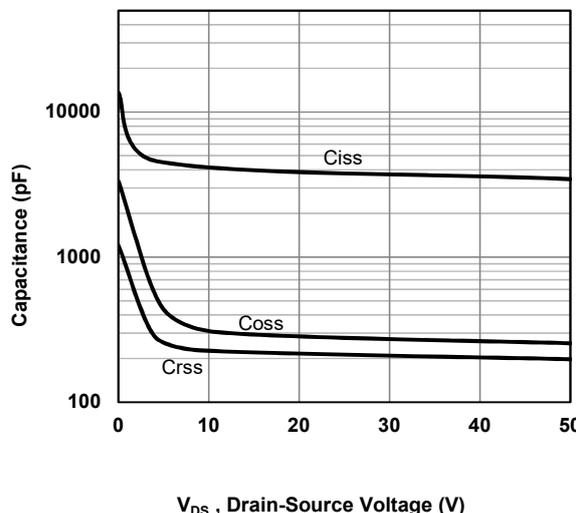
### Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Symbol	Parameter	Rating	Unit
<b>Common Ratings (TC=25°C Unless Otherwise Noted)</b>			
V <sub>DS</sub>	Drain-Source Breakdown Voltage	60	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
T <sub>J</sub>	Maximum Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature Range	-50 to 155	°C
I <sub>S</sub>	Diode Continuous Forward Current	Tc=25°C 85	A
<b>Mounted on Large Heat Sink</b>			
I <sub>DM</sub>	Pulse Drain Current Tested	Tc=25°C 340	A
I <sub>D</sub>	Continuous Drain Current	Tc=25°C 85	A
P <sub>D</sub>	Maximum Power Dissipation	Tc=25°C 50	W
R <sub>θJA</sub>	Thermal Resistance Junction-to-Ambient	40	°C/W

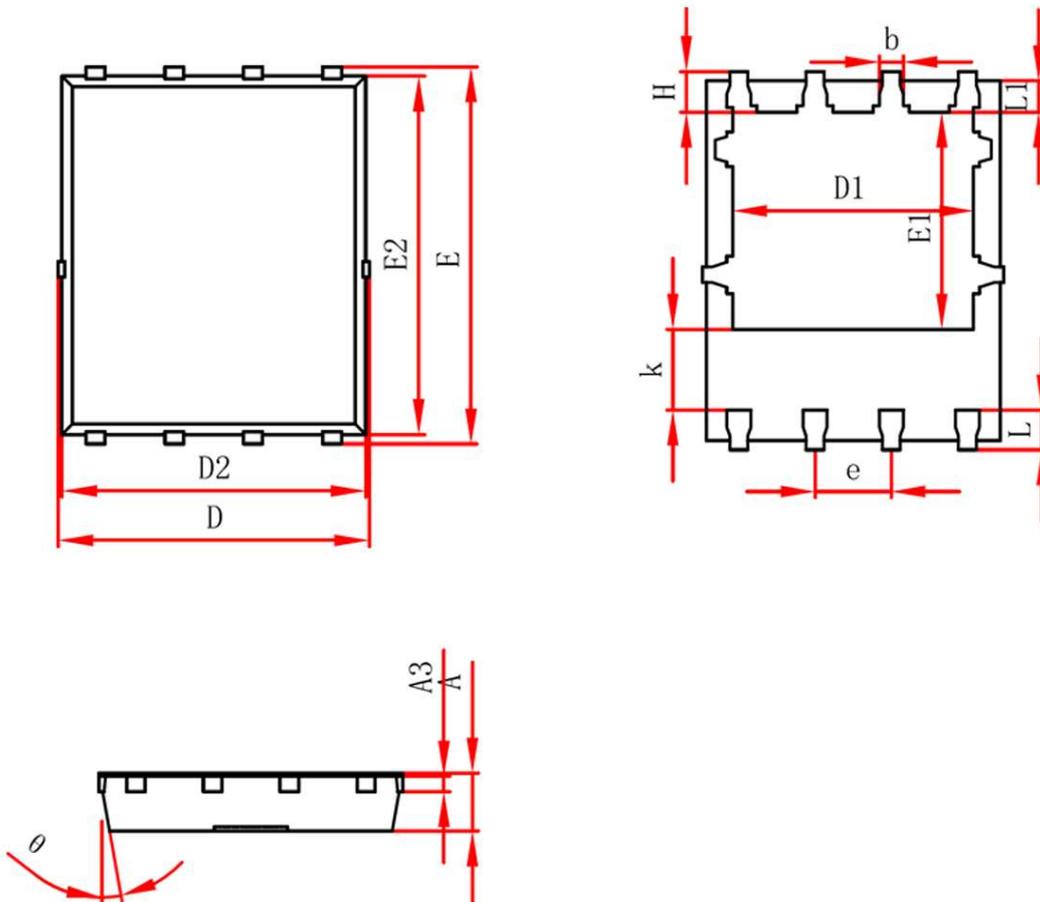
### Ordering Information (Example)

Type	Package	Marking	Minimum Package(pcs)	Inner Box Quantity(pcs)	Outer Carton Quantity(pcs)	Delivery Mode
MC0G85AG	PDFN5X6-8L	MC0G85AG	5,000	10,000	70,000	13"reel

Electrical Characteristics (T <sub>J</sub> =25°C unless otherwise noted)						
Symbol	Parameter	Condition	Min	Typ	Max	Unit
<b>Static Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
B <sub>V(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	60	--	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V	--	--	1	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	--	--	±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2	--	4	V
R <sub>DS(on)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	--	5	6.2	mΩ
<b>Dynamic Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
C <sub>ISS</sub>	Input Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHz	--	3782	--	pF
C <sub>OSS</sub>	Output Capacitance		--	297	--	pF
C <sub>RSS</sub>	Reverse Transfer Capacitance		--	218	--	pF
<b>Switching Characteristics</b>						
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =30V, I <sub>D</sub> =20A, V <sub>GS</sub> =10V	--	77	--	nC
Q <sub>gs</sub>	Gate Source Charge		--	21	--	nC
Q <sub>gd</sub>	Gate Drain Charge		--	24	--	nC
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DS</sub> =30V, I <sub>D</sub> =20A, V <sub>GS</sub> =10V, R <sub>G</sub> =1.8Ω	--	18	--	nS
t <sub>r</sub>	Turn-on Rise Time		--	88	--	nS
t <sub>d(off)</sub>	Turn-Off Delay Time		--	37	--	nS
t <sub>f</sub>	Turn-Off Fall Time		--	85	--	nS
<b>Source- Drain Diode Characteristics</b>						
V <sub>SD</sub>	Forward on voltage	T <sub>J</sub> =25°C, I <sub>S</sub> =10A	--	--	1.2	V

**Typical Operating Characteristics**

**Fig1. Typical Output Characteristics**

**Fig2. Typical Gate Charge Vs. Gate-Source Voltage**

**Fig3. Normalized On-Resistance Vs. Temperature**

**Fig4. On-Resistance Vs. Drain-Source Current**

**Fig5. Maximum Safe Operating Area**

**Fig6 Typical Capacitance Vs. Drain-Source**

## PDFN5X6-8L Package information



Symbol	Dimensions in Millimeters(mm)		Dimensions In Inches	
	Min	Max	Min	Max
A	0.950	1.050	0.035	0.039
A3	0.254REF.		0.010REF.	
D	4.950	5.050	0.196	0.200
E	5.950	6.050	0.235	0.239
D1	4.026	4.126	0.159	0.163
E1	3.510	3.610	0.139	0.143
D2	4.850	4.950	0.192	0.196
E2	5.700	5.800	0.225	0.229
k	1.190	1.390	0.047	0.055
b	0.300	0.400	0.012	0.016
e	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
$\theta$	10°	12°	10°	12°