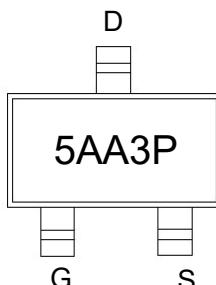


Features

- P-Channel
- High density cell design for ultra low Rdson
- Enhancement mode
- Logic Level
- dv/dt rated

Application

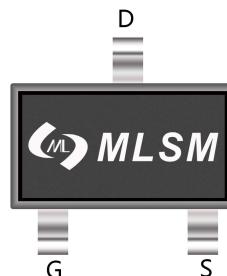
- PWM applications
- Power management
- Load switch



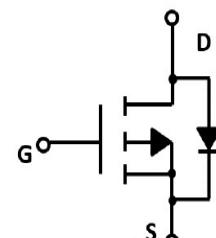
5AA3P: Device code

Product Summary

V _{DS}	R _{DS(ON)} MAX	I _D MAX
-500V	150Ω@-10V	-0.3A



SOT-23-3L top view



Schematic diagram

Marking and pin assignment



Halogen-Free

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Symbol	Parameter	Rating	Unit
Common Ratings (TC=25°C Unless Otherwise Noted)			
V _{DS}	Drain-Source Breakdown Voltage	-500	V
V _{GS}	Gate-Source Voltage	±20	V
T _J	Maximum Junction Temperature	150	°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
I _S	Diode Continuous Forward Current	Tc=25°C -0.3	A
Mounted on Large Heat Sink			
I _{DM}	Pulse Drain Current Tested	Tc=25°C -1	A
I _D	Continuous Drain Current	Tc=25°C -0.3	A
P _D	Maximum Power Dissipation	Tc=25°C 1	W
R _{θJA}	Thermal Resistance Junction-to-Ambient	125	°C/W

Ordering Information (Example)

Type	Package	Marking	Minimum Package(pcs)	Inner Box Quantity(pcs)	Outer Carton Quantity(pcs)	Delivery Mode
MLSK5AA3P	SOT-23-3L	5AA3P	3,000	45,000	180,000	7"reel

Electrical Characteristics (TJ=25°C unless otherwise noted)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ TJ = 25°C (unless otherwise stated)						
$BV_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-500	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=-500V, V_{GS}=0V$	--	--	-1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	--	--	± 100	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1	-1.8	-2.5	V
$R_{DS(on)}$	Drain-Source On-State Resistance	$V_{GS}=-10V, I_D=-0.2A$	--	90	150	Ω

Dynamic Electrical Characteristics @ TJ = 25°C (unless otherwise stated)

C_{ISS}	Input Capacitance	$V_{DS}=-25V, V_{GS}=0V, f=1MHz$	--	36.5	--	pF
C_{OSS}	Output Capacitance		--	7	--	pF
C_{RSS}	Reverse Transfer Capacitance		--	1.55	--	pF

Switching Characteristics

Q_g	Total Gate Charge	$V_{DS}=-25V, I_D=-0.2A, V_{GS}=-10V$	--	2.1	--	nC
Q_{gs}	Gate Source Charge		--	0.65	--	nC
Q_{gd}	Gate Drain Charge		--	0.25	--	nC
$t_{d(on)}$	Turn-on Delay Time	$V_{DD}=-25V, I_D=-0.2A, V_{GS}=-10V, R_G=3.3\Omega$	--	5.2	--	nS
t_r	Turn-on Rise Time		--	7.5	--	nS
$t_{d(off)}$	Turn-Off Delay Time		--	8.4	--	nS
t_f	Turn-Off Fall Time		--	2.8	--	nS

Source- Drain Diode Characteristics

V_{SD}	Forward on voltage	$T_j=25^\circ C, I_s=-0.2A$	--	--	-1.2	V
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Typical Operating Characteristics

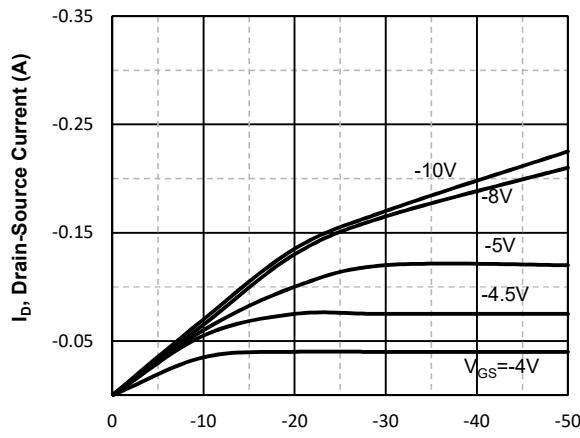


Fig1. Typical Output Characteristics

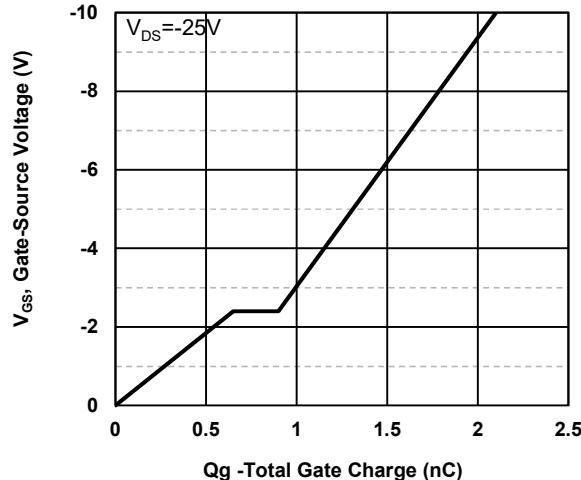


Fig2. Typical Gate Charge Vs.Gate-Source Voltage

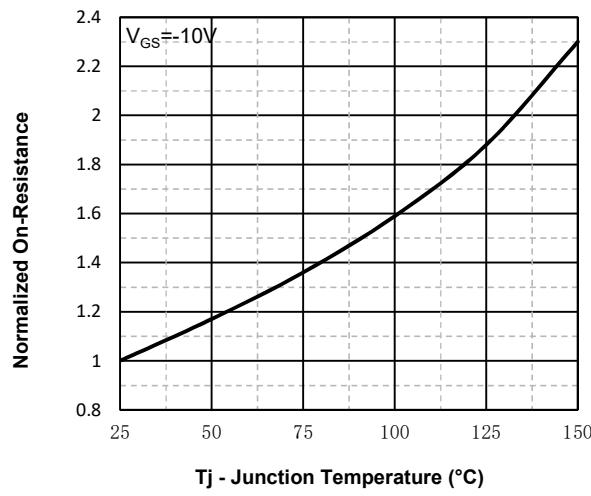


Fig3. Normalized On-Resistance Vs. Temperature

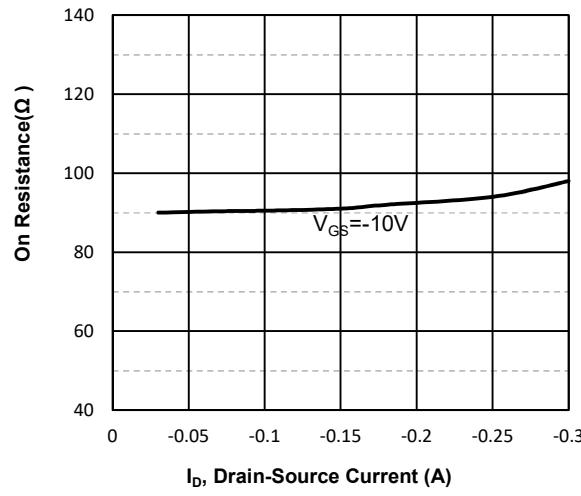


Fig4. On Resistance Vs.Drain-Source Current

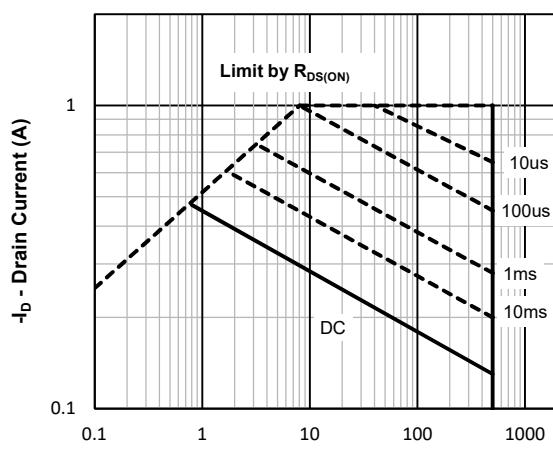


Fig5. Maximum Safe Operating Area

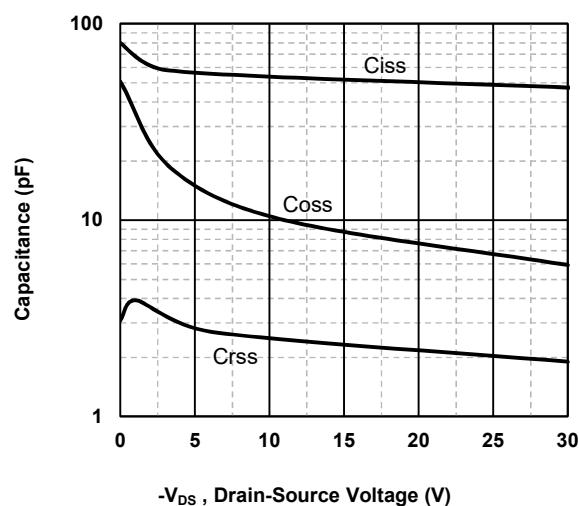
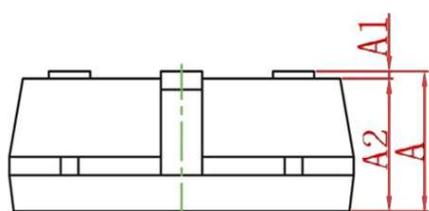
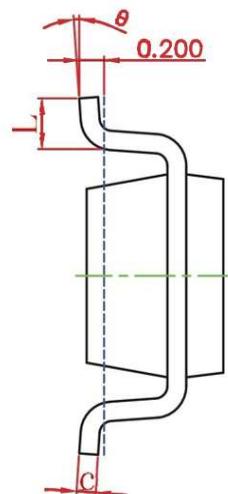
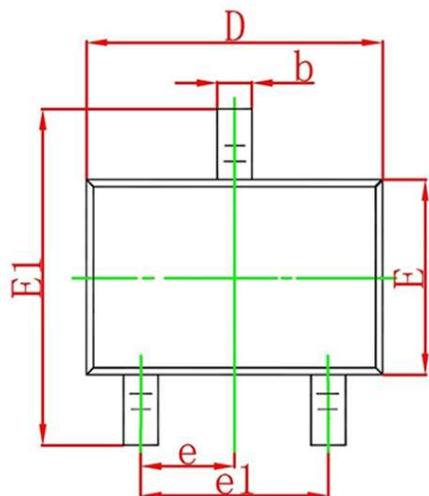


Fig6. Typical Capacitance Vs.Drain-Source Voltage

SOT-23-3L Package information


Symbol	Dimensions in Millimeters(mm)		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.042	0.050
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.042	0.046
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.112	0.120
E	1.500	1.700	0.060	0.068
E1	2.650	2.950	0.106	0.118
e	0.950TYP		0.037TYP	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°