

Features

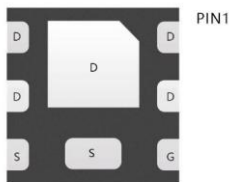
- High density cell design for ultra low $R_{DS(on)}$
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation

Product Summary

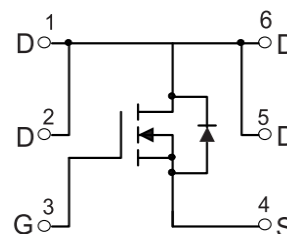
V_{DS}	$R_{DS(ON)}$ TYP	I_D
40V	27mΩ@10V	8A
	35mΩ@4.5V	

Application

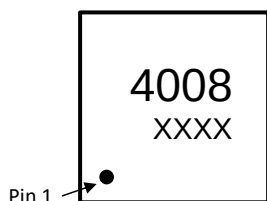
- Load switching
- Hard switched and high frequency circuits
- Uninterruptible power supply



DFN2X2-6L view



Schematic diagram



Pin 1

4008 : Device code
 XXXX: Code
 Solid dot: Pin1 indicator

Marking and pin assignment



Halogen-Free

Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Rating	Unit
Common Ratings ($T_C=25^{\circ}\text{C}$ Unless Otherwise Noted)			
V_{DS}	Drain-Source Breakdown Voltage	40	V
V_{GS}	Gate-Source Voltage	± 20	V
T_J	Maximum Junction Temperature	150	$^{\circ}\text{C}$
T_{STG}	Storage Temperature Range	-50 to 155	$^{\circ}\text{C}$
I_S	Diode Continuous Forward Current	$T_C=25^{\circ}\text{C}$ 8	A

Mounted on Large Heat Sink

I_{DM}	Pulse Drain Current Tested	$T_C=25^{\circ}\text{C}$ 40	A
I_D	Continuous Drain Current	$T_C=25^{\circ}\text{C}$ 8	A
P_D	Maximum Power Dissipation	$T_C=25^{\circ}\text{C}$ 1.96	W
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	83.5	$^{\circ}\text{C/W}$

Ordering Information (Example)

Type	Package	Marking	Minimum Package(pcs)	Inner Box Quantity(pcs)	Outer Carton Quantity(pcs)	Delivery Mode
MLSM4008	DFN2X2-6L	4008	3,000	45,000	180,000	7"reel

Electrical Characteristics (TJ=25°C unless otherwise noted)						
Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ TJ = 25°C (unless otherwise stated)						
BV _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	40	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =40V, V _{GS} =0V	--	--	1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V	--	--	±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	0.8	--	2.0	V
R _{DS(on)}	Drain-Source On-State Resistance	V _{GS} =10V, I _D =8A	--	27	35	mΩ
		V _{GS} =4.5V, I _D =5A	--	35	50	mΩ
Dynamic Electrical Characteristics @ TJ = 25°C (unless otherwise stated)						
C _{ISS}	Input Capacitance	V _{DS} =20V, V _{GS} =0V, f=1MHz	--	530	--	pF
C _{OSS}	Output Capacitance		--	68	--	pF
C _{RSS}	Reverse Transfer Capacitance		--	58	--	pF
Switching Characteristics						
Q _g	Total Gate Charge	V _{DD} =20V, I _D =8A, V _{GS} =10V	--	15.5	--	nC
Q _{gs}	Gate Source Charge		--	2.9	--	nC
Q _{gd}	Gate Drain Charge		--	3.5	--	nC
t _{d(on)}	Turn-on Delay Time	V _{DD} =10V, I _D =8A, V _{GS} =4.5V, R _G =3Ω	--	10	--	nS
t _r	Turn-on Rise Time		--	8	--	nS
t _{d(off)}	Turn-Off Delay Time		--	25	--	nS
t _f	Turn-Off Fall Time		--	10	--	nS
Source- Drain Diode Characteristics						
V _{SD}	Forward on voltage	Tj=25°C, Is=8A	--	--	1.2	V

Typical Operating Characteristics

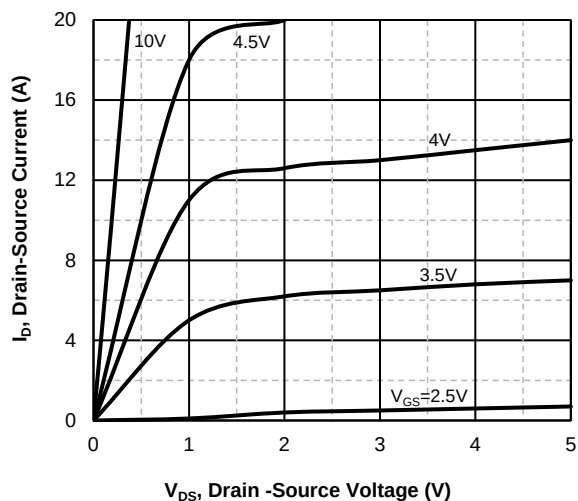


Fig1. Typical Output Characteristics

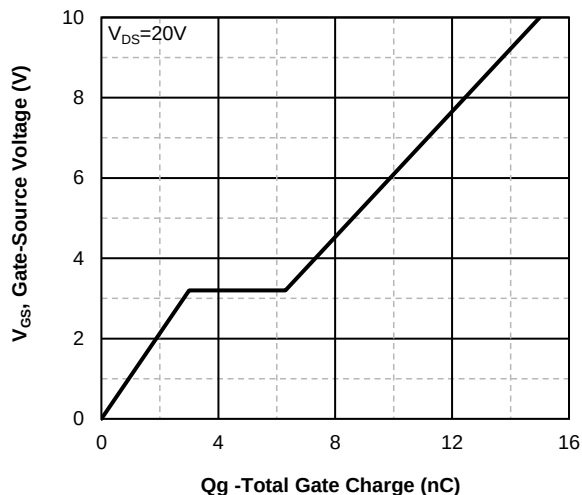


Fig2. Typical Gate Charge Vs. Gate-Source Voltage

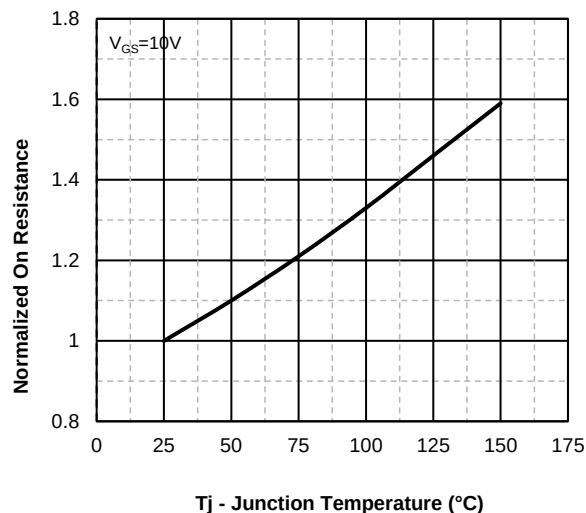


Fig3. Normalized On-Resistance Vs. Temperature

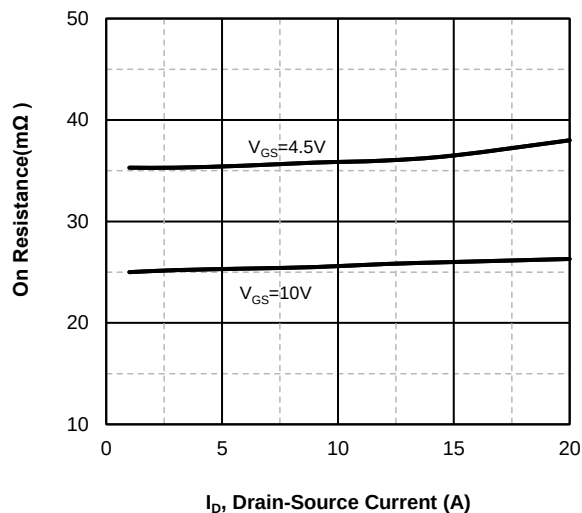


Fig4. On-Resistance Vs. Drain-Source Current

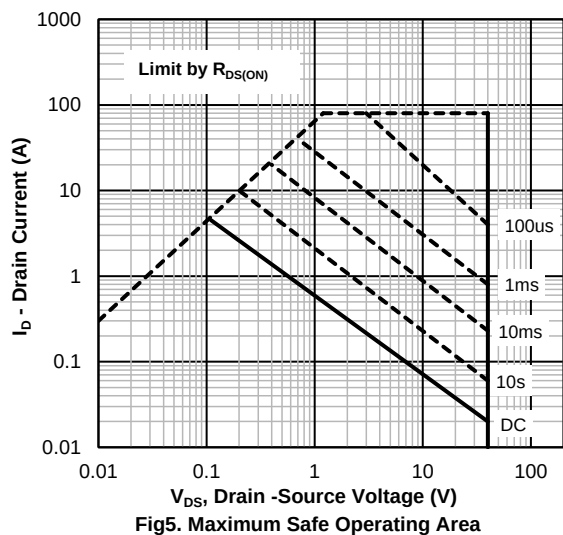


Fig5. Maximum Safe Operating Area

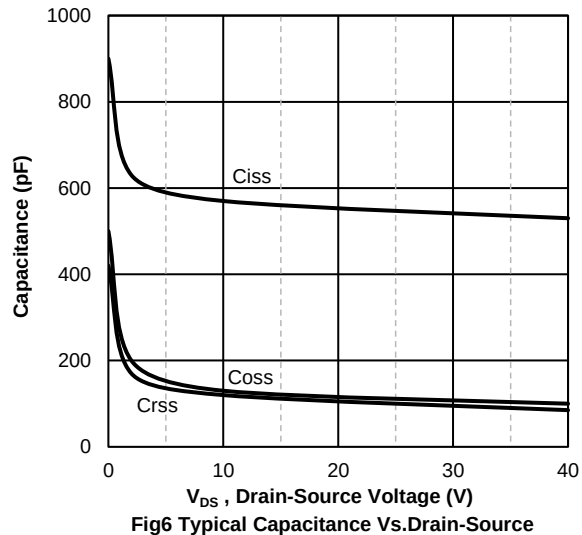
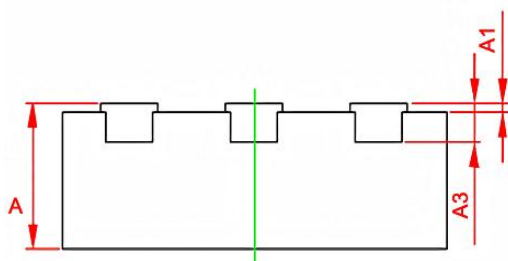
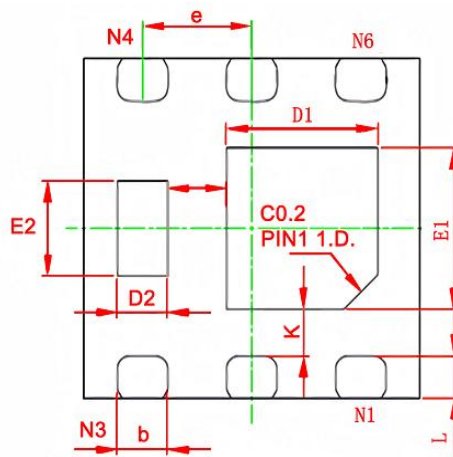
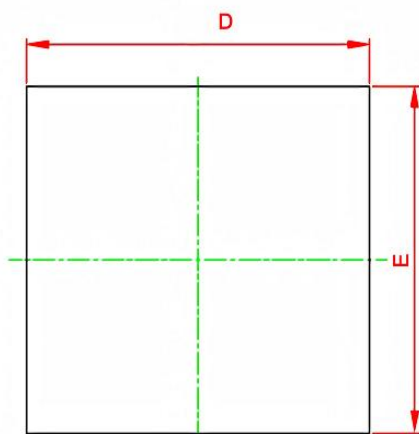


Fig6. Typical Capacitance Vs. Drain-Source

DFN2X2-6L Package information



Symbol	Dimensions in Millimeters(mm)		Dimensions In Inches	
	Min	Max	Min	Max
A	0.600	0.700	0.023	0.027
A1	0.000	0.050	0.000	0.001
A3	0.203REF		0.007REF	
b	0.315	0.415	0.012	0.016
D	1.924	2.076	0.075	0.081
E	1.924	2.076	0.075	0.081
e	0.650TYP		0.225TYP	
L	0.224	0.376	0.008	0.014
k	0.200	-	0.007	-
E1	1.000	1.200	0.039	0.047
D1	0.900	1.100	0.035	0.043
E2	0.700	0.900	0.027	0.035
D2	0.150	0.350	0.005	0.013