

Features

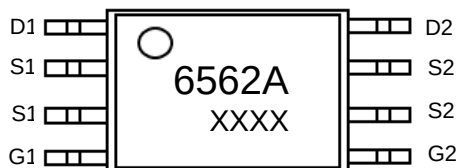
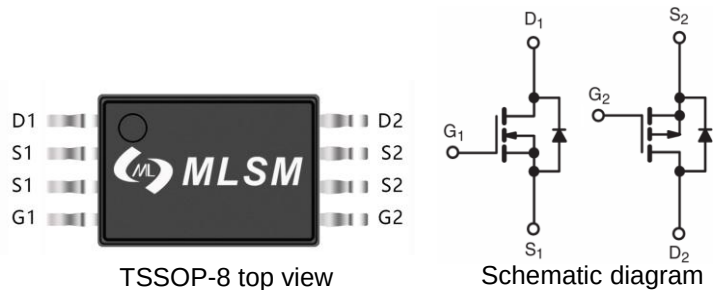
- Trench Power LV MOSFET technology
- High density cell design for Low $R_{DS(ON)}$
- High Speed switching

Application

- DC/DC converter
- Load switch
- LCD display inverter

Product Summary

V_{DS}	$R_{DS(ON)}$ TYP	I_D
20V	18m Ω @4.5V	6.7A
	29m Ω @2.5V	
-20V	24m Ω @-4.5V	-6.1A
	36m Ω @-2.5V	



Marking and Pin assignment

6562A: Device code
XXXX: Code



Halogen-Free

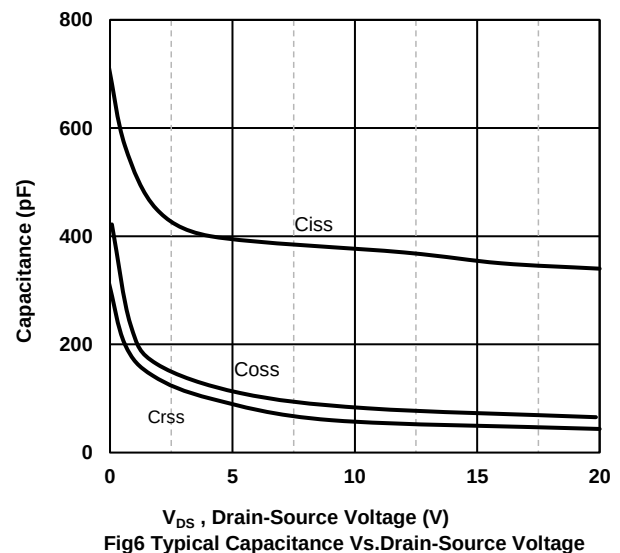
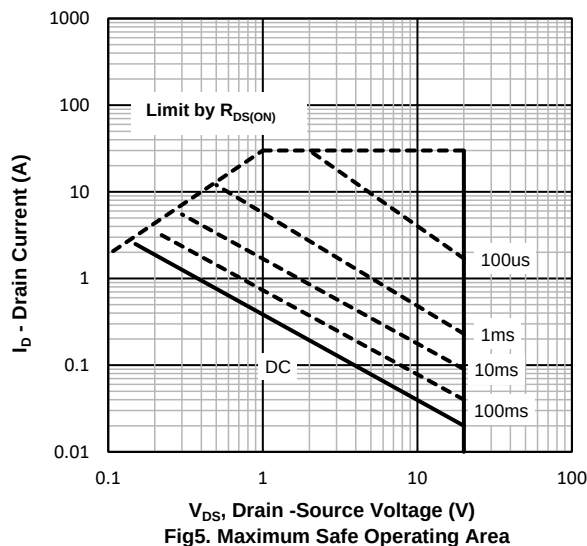
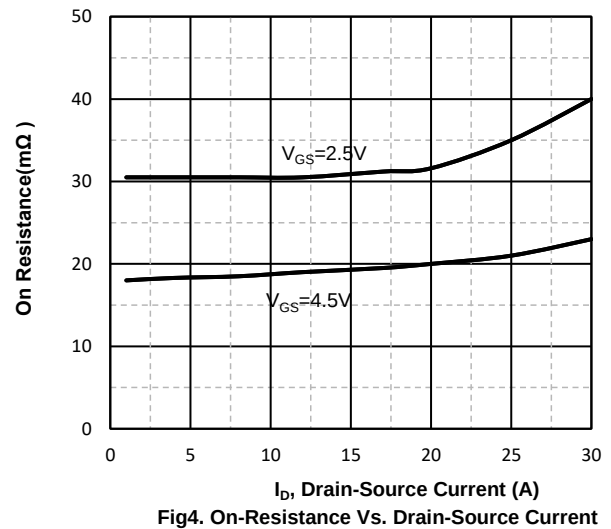
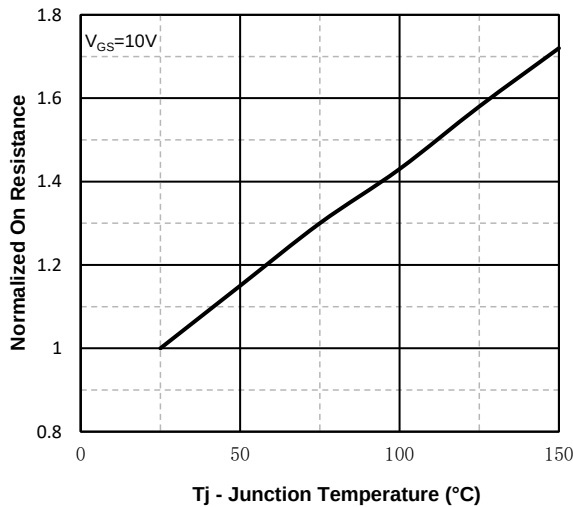
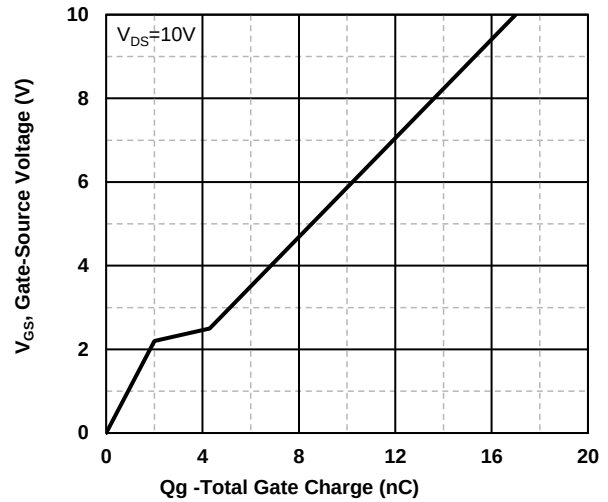
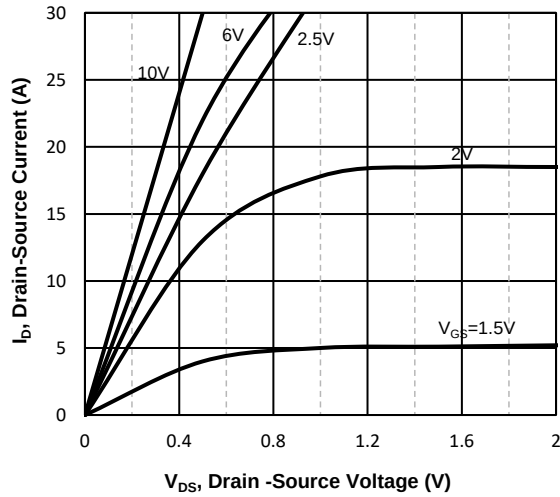
Absolute Maximum Ratings (TA=25°C unless otherwise noted)					
Symbol	Parameter		N-Channel	P-Channel	Unit
Common Ratings (TC=25°C Unless Otherwise Noted)					
V_{DS}	Drain-Source Breakdown Voltage		20	-20	V
V_{GS}	Gate-Source Voltage		±12	±12	V
T_J	Maximum Junction Temperature		150	150	°C
T_{STG}	Storage Temperature Range		-55 to 150	-55 to 150	°C
I_S	Diode Continuous Forward Current	Tc=25°C	6.7	-6.1	A
Mounted on Large Heat Sink					
I_{DM}	Pulse Drain Current Tested	Tc=25°C	30	-30	A
I_D	Continuous Drain Current	Tc=25°C	6.7	-6.1	A
P_D	Maximum Power Dissipation	Tc=25°C	1.6	1.7	W
$R_{\theta JA}$	Thermal Resistance Junction-Ambient		225	225	°C/W

Ordering Information (Example)						
Type	Package	Marking	Minimum Package(pcs)	Inner Box Quantity(pcs)	Outer Carton Quantity(pcs)	Delivery Mode
MS6562A	TSSOP-8	6562A	3,000	6,000	42,000	13"reel

N-Ch Electrical Characteristics (TJ=25℃ unless otherwise noted)						
Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ TJ = 25℃ (unless otherwise stated)						
BV _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	20	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =20V, V _{GS} =0V	--	--	1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±12V, V _{DS} =0V	--	--	±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	0.6	0.75	1.5	V
R _{DS(on)}	Drain-Source On-State Resistance	V _{GS} =4.5V, I _D =5.7A	--	18	22	mΩ
		V _{GS} =2.5V, I _D =4.4A	--	29	36	mΩ
Dynamic Electrical Characteristics @ TJ = 25℃ (unless otherwise stated)						
C _{ISS}	Input Capacitance	V _{DS} =10V, V _{GS} =0V, f=1MHz	--	378	--	pF
C _{OSS}	Output Capacitance		--	74	--	pF
C _{RSS}	Reverse Transfer Capacitance		--	58	--	pF
Switching Characteristics						
Q _g	Total Gate Charge	V _{DS} =10V, I _D =6.7A, V _{GS} =4.5V	--	6.05	--	nC
Q _{gs}	Gate Source Charge		--	1.07	--	nC
Q _{gd}	Gate Drain Charge		--	1.95	--	nC
t _{d(on)}	Turn-on Delay Time	V _{DS} =10V, R _L =2Ω, V _{GS} =4.5V, R _G =3Ω	--	4.2	--	nS
t _r	Turn-on Rise Time		--	19.8	--	nS
t _{d(off)}	Turn-Off Delay Time		--	22.6	--	nS
t _f	Turn-Off Fall Time		--	23.2	--	nS
Source- Drain Diode Characteristics						
V _{SD}	Forward on voltage	Tj=25℃, Is=6.7A	--	--	1.2	V

P-Ch Electrical Characteristics (TJ=25℃ unless otherwise noted)						
Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ TJ = 25℃ (unless otherwise stated)						
BV _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250μA	-20	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-20V, V _{GS} =0V	--	--	-1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±12V, V _{DS} =0V	--	--	±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250μA	-0.4	-0.7	-1.5	V
R _{DS(on)}	Drain-Source On-State Resistance	V _{GS} =-4.5V, I _D =-5.1A	--	24	30	mΩ
		V _{GS} =-2.5V, I _D =-4.2A	--	36	45	mΩ
Dynamic Electrical Characteristics @ TJ = 25℃ (unless otherwise stated)						
C _{ISS}	Input Capacitance	V _{DS} =-10V, V _{GS} =0V, f=1MHz	--	1015	--	pF
C _{OSS}	Output Capacitance		--	138	--	pF
C _{RSS}	Reverse Transfer Capacitance		--	105	--	pF
Switching Characteristics						
Q _g	Total Gate Charge	V _{DS} =-10V, I _D =-6.1A, V _{GS} =-4.5V	--	11.3	--	nC
Q _{gs}	Gate Source Charge		--	2.3	--	nC
Q _{gd}	Gate Drain Charge		--	2.4	--	nC
t _{d(on)}	Turn-on Delay Time	V _{DD} =-10V, I _D =-6.1A, V _{GS} =-4.5V, R _G =2.5Ω	--	8.5	--	nS
t _r	Turn-on Rise Time		--	35.5	--	nS
t _{d(off)}	Turn-Off Delay Time		--	78	--	nS
t _f	Turn-Off Fall Time		--	58	--	nS
Source- Drain Diode Characteristics						
V _{SD}	Forward on voltage	Tj=25℃, Is=-6.1A	--	--	-1.2	V

N-Channel Typical Operating Characteristics



P-Channel Typical Operating Characteristics

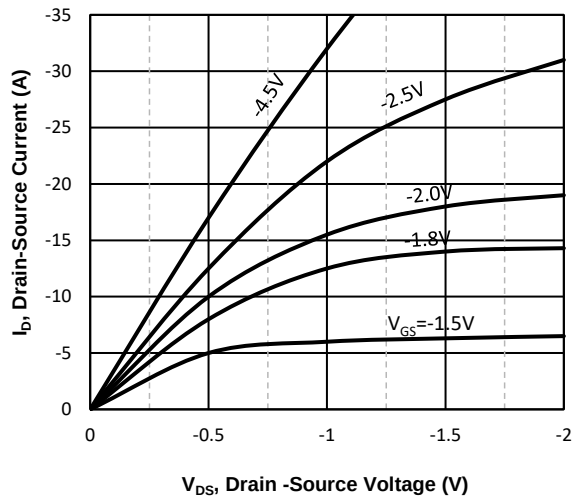


Fig7. Typical Output Characteristics

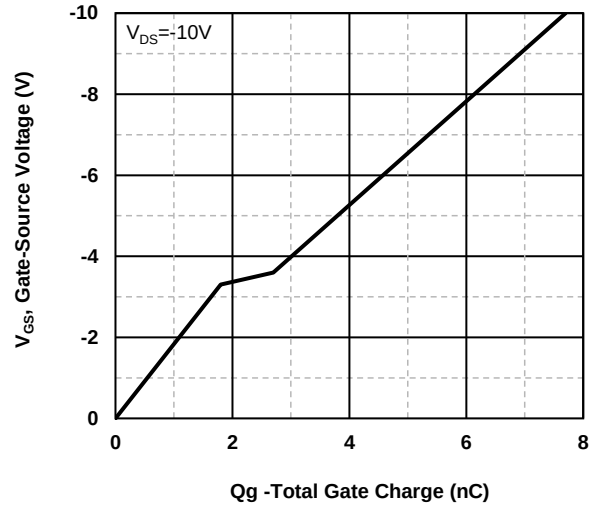


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

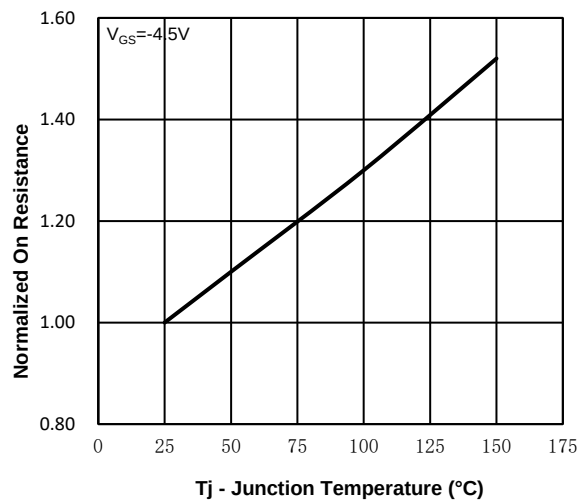


Fig9. Normalized On-Resistance Vs. Temperature

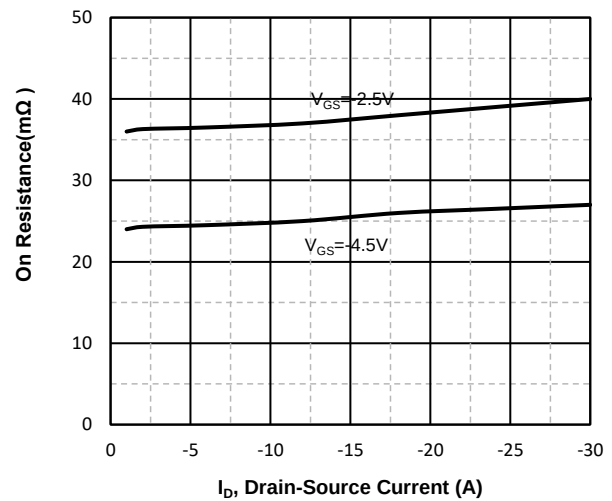


Fig10. On-Resistance Vs. Drain-Source Current

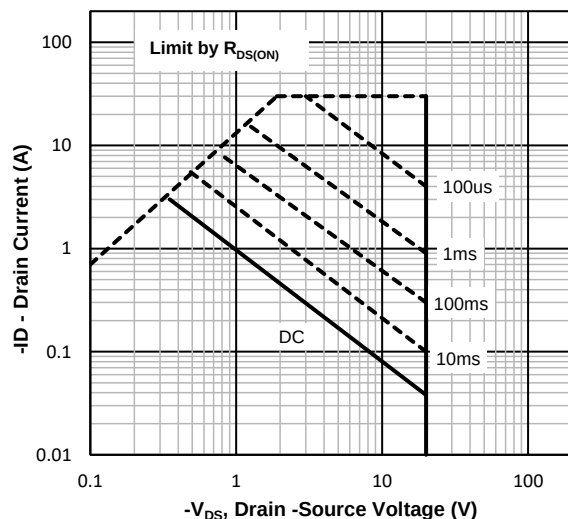


Fig11. Maximum Safe Operating Area

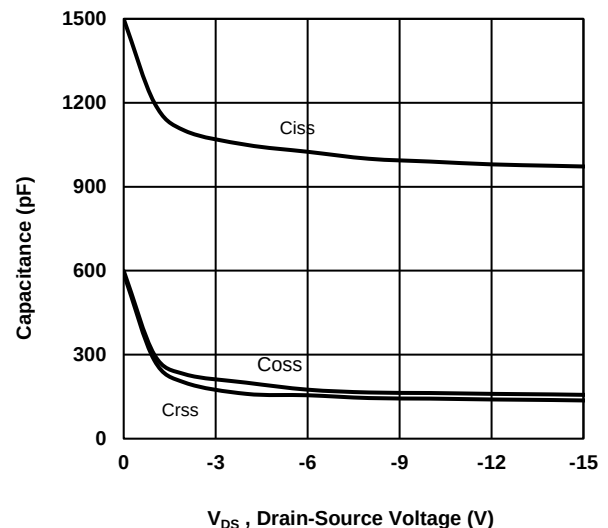
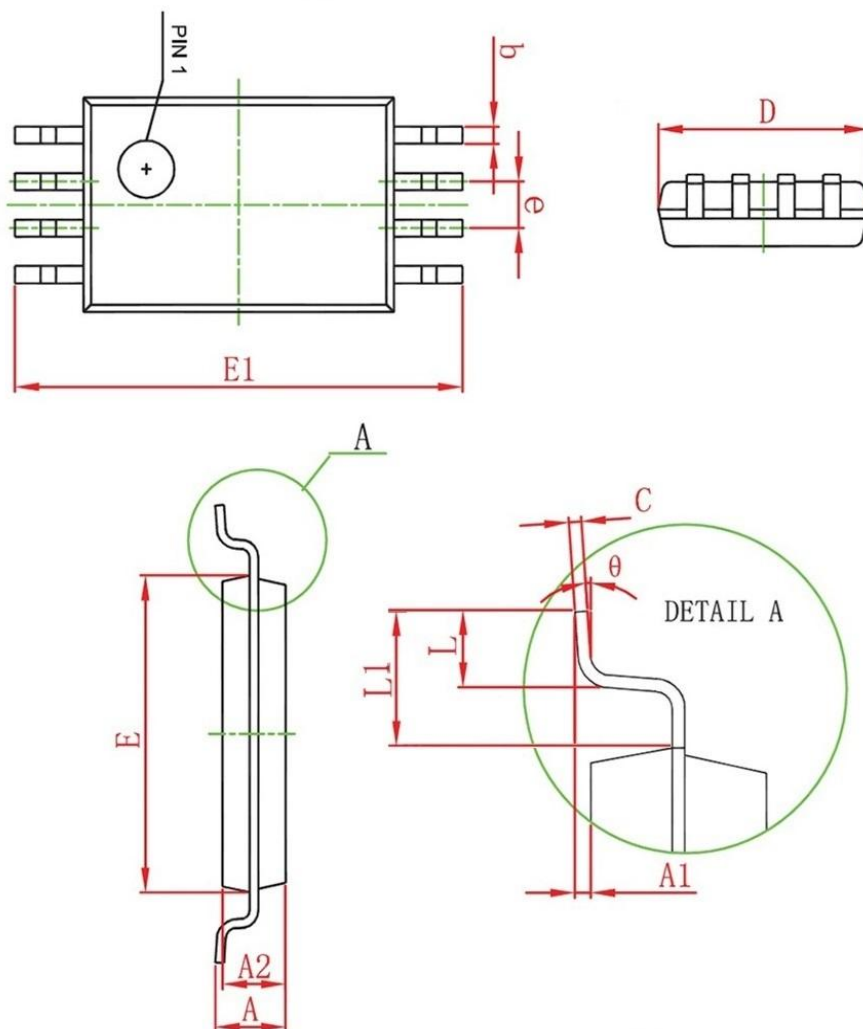


Fig12. Typical Capacitance Vs. Drain-Source Voltage

TSSOP-8 Package information



Symbol	Dimensions in Millimeters(mm)		Dimensions in Inches	
	Min	Max	Min	Max
A	1.000	1.200	0.039	0.047
A1	0.020	0.180	0.000	0.007
A2	0.900	1.100	0.035	0.043
b	0.170	0.270	0.006	0.010
c	0.122	0.132	0.004	0.005
D	2.870	3.070	0.112	0.120
e	0.65BSC		0.025BSC	
E	4.300	4.500	0.169	0.177
E1	6.200	6.600	0.244	0.259
L	0.400	0.800	0.015	0.031
L1	1.00BSC		0.039BSC	
Ø1	0.500	0.700	0.001	0.027
θ	0°	10°	0°	10°